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Bibliography

PWS00: , Telnet commands specifications, 2017

1. Purpose of this document

The purpose of this document is to depict communication protocol used to communicate with Mezzo MCU.

1.1 Definitions

Equipment or amplifier is the Mezzo

Remote controller is the software, running on a PC or on another device, that is enabled to communicate with Equipment

2. Protocol description

The protocol is structured as master/slave where the remote control program running on a PC is the master and the amplifier is slave.

Communication will take place on a LAN Ethernet infrastructure, and uses UDP packet. **All packets must be sent to UDP port 8002**

All data bigger than 1 byte are represented in **little endian** if not otherwise specified, please see the example below that explain the endianness.

Example [edit | edit wikitext]

In the case of a WORD (16 bit), the number hexadecimal 0x0123 will be stored as:

	Little endian	Big endian
	+-----+	+-----+
	0x23 0x01	0x01 0x23
	+-----+	+-----+
byte:	0 1	0 1

2.1. Packet structure

The protocol is asymmetrical: the request and response frame are slightly different.

2.1.1. Request frame

All packets sent from PC to a device will be further defined request. A generic request packet is composed by the following fields:

Head	TAG	PBus command	...	CRC16	Tail
1 byte	4 bytes	variable	...	2 bytes	1 byte

where:

- **Head** is a fixed value, corresponding to the ASCII code **STX (0x02)**.
- **TAG** is a binary tag to be echoed back into reply, used by software to associate the reply with the issued command.
- **PBus** command is a single encapsulation of a PBus operation. In a Pbus frame can be encapsulated more PBus command, this feature is called Multicommand. The PBus command will be explained in section Error: sorgente del riferimento non trovata.
- **CRC16** is a 2 bytes value, computed as the rule presented in the paragraph ##. It is computed only on the grey part of the frame.
- **Tail** is a fixed value, corresponding to ASCII code **ETX (0x03)**.

Since some packets could contain binary data, an escaping strategy has been adopted, refer to the paragraph ## for more information. The escaping is adopted for the whole frame except Head and Tail. As a consequence CRC16 is computed before escaping.

A request packet can be as long as 2000 byte (plain bytes not escaped), as a consequence 1992 bytes are available for pbus commands.

2.1.2. Response frame

All packets received from the PC (sent by a device), will be further defined replies. A reply has always the same TAG of the command that generated it and has the following structure:

Head	Magic Number	ProtocolID	TAG	PBus command Reply	...	CRC16	Tail
1 byte	3 bytes	2 bytes	4 bytes	variable	...	2 bytes	1 byte

where:

- **Head** is a fixed value, corresponding to the ASCII code **STX (0x02)**.
- **Magic Number** is 3 byte field that must be populated with 'M', 'Z', 'O' (0x4D, 0x5A, 0x4F).
- **Protocol ID** is an identification of protocol frame such as version etc. Mezzo uses protocol identified by **0x0001**.
- **TAG** is a binary tag echoed back from the last issued command.
- **PBus command reply** is a single encapsulation of a PBus operation reply. In multicommand contest every PBus command encapsulated in a single protocol frame needs a PBus command reply encapsulated in a reply.
- **CRC16** is a 2 bytes value, computed as the rule presented in the paragraph Error: sorgente del riferimento non trovata. It is computed only on the grey part of the frame.
- **Tail** is a fixed value, corresponding to ASCII code **ETX (0x03)**.

2.2. Escaping strategy

Since the protocol is binary some byte in the frame could assume the same value of **STX** or **ETX**, so an escaping strategy will take place.

In order to avoid the transmission of the special bytes **STX**, **ETX** and **ESC** an escaping strategy has been adopted.

In case a special byte is found within the packet, an **ESC (0x1B)** character is issued in the output buffer followed by the special byte incremented by **0x40** (i.e. **STX** becomes **ESC** followed by **0x42**, since 0x42 is the sum of STX [0x02] and 0x40).

The escaping strategy involves all fields of packets (request or response), excluding **Head** and **Tail**.

Below a code snippet shows an example of the escaping and un-escaping routines:

2.3 CRC16 computation

Each packet contains a CRC16. The checksum value is computed before applying the escaping strategy. The CRC algorithm is the CRC16-CCITT that uses $x^{16}+x^{12}+x^5+1$ polynomial

A code snippet used to compute CRC16 is provided below:

```
const uint16_t crc16tab[256] = {
    0x0000, 0x1021, 0x2042, 0x3063, 0x4084, 0x50a5, 0x60c6, 0x70e7,
    0x8108, 0x9129, 0xa14a, 0xb16b, 0xc18c, 0xd1ad, 0xe1ce, 0xf1ef,
    0x1231, 0x0210, 0x3273, 0x2252, 0x52b5, 0x4294, 0x72f7, 0x62d6,
    0x9339, 0x8318, 0xb37b, 0xa35a, 0xd3bd, 0xc39c, 0xf3ff, 0xe3de,
    0x2462, 0x3443, 0x0420, 0x1401, 0x64e6, 0x74c7, 0x44a4, 0x5485,
    0xa56a, 0xb54b, 0x8528, 0x9509, 0xe5ee, 0xf5cf, 0xc5ac, 0xd58d,
    0x3653, 0x2672, 0x1611, 0x0630, 0x76d7, 0x66f6, 0x5695, 0x46b4,
    0xb75b, 0xa77a, 0x9719, 0x8738, 0xf7df, 0xe7fe, 0xd79d, 0xc7bc,
    0x48c4, 0x58e5, 0x6886, 0x78a7, 0x0840, 0x1861, 0x2802, 0x3823,
    0xc9cc, 0xd9ed, 0xe98e, 0xf9af, 0x8948, 0x9969, 0xa90a, 0xb92b,
    0x5af5, 0x4ad4, 0x7ab7, 0x6a96, 0x1a71, 0x0a50, 0x3a33, 0x2a12,
    0xdbfd, 0xcdbd, 0xfbbf, 0xeb9e, 0x9b79, 0x8b58, 0xbb3b, 0xab1a,
    0x6ca6, 0x7c87, 0x4ce4, 0x5cc5, 0x2c22, 0x3c03, 0x0c60, 0x1c41,
    0xedeae, 0xfdf8f, 0xcddec, 0xddcd, 0xad2a, 0xbd0b, 0x8d68, 0x9d49,
    0x7e97, 0x6eb6, 0x5ed5, 0x4ef4, 0x3e13, 0x2e32, 0x1e51, 0x0e70,
    0xff9f, 0xefbe, 0xdfdd, 0xcffc, 0xbf1b, 0xaf3a, 0x9f59, 0x8f78,
    0x9188, 0x81a9, 0xb1ca, 0xa1eb, 0xd10c, 0xc12d, 0xf14e, 0xe16f,
    0x1080, 0x00a1, 0x30c2, 0x20e3, 0x5004, 0x4025, 0x7046, 0x6067,
    0x83b9, 0x9398, 0xa3fb, 0xb3da, 0xc33d, 0xd31c, 0xe37f, 0xf35e,
    0x02b1, 0x1290, 0x22f3, 0x32d2, 0x4235, 0x5214, 0x6277, 0x7256,
    0xb5ea, 0xa5cb, 0x95a8, 0x8589, 0xf56e, 0xe54f, 0xd52c, 0xc50d,
    0x34e2, 0x24c3, 0x14a0, 0x0481, 0x7466, 0x6447, 0x5424, 0x4405,
    0xa7db, 0xb7fa, 0x8799, 0x97b8, 0xe75f, 0xf77e, 0xc71d, 0xd73c,
    0x26d3, 0x36f2, 0x0691, 0x16b0, 0x6657, 0x7676, 0x4615, 0x5634,
    0xd94c, 0xc96d, 0xf90e, 0xe92f, 0x99c8, 0x89e9, 0xb98a, 0xa9ab,
    0x5844, 0x4865, 0x7806, 0x6827, 0x18c0, 0x08e1, 0x3882, 0x28a3,
    0xcb7d, 0xdb5c, 0xeb3f, 0xfb1e, 0x8bf9, 0x9bd8, 0xabbb, 0xbb9a,
    0x4a75, 0x5a54, 0x6a37, 0x7a16, 0x0af1, 0x1ad0, 0x2ab3, 0x3a92,
    0xfd2e, 0xed0f, 0xdd6c, 0xcd4d, 0xbdaa, 0xad8b, 0x9de8, 0x8dc9,
    0x7c26, 0x6c07, 0x5c64, 0x4c45, 0x3ca2, 0x2c83, 0x1ce0, 0x0cc1,
    0xef1f, 0xff3e, 0xcf5d, 0xdf7c, 0xaf9b, 0xbfba, 0x8fd9, 0x9ff8,
    0x6e17, 0x7e36, 0x4e55, 0x5e74, 0x2e93, 0x3eb2, 0x0ed1, 0x1ef0
};

uint16_t crc16(uint16_t crc, const void *buffer, size_t len)
{
    const unsigned char *buf = (const unsigned char *)buffer;
    while(len--)
        crc = (crc16tab[((crc) >> 8) ^ ((unsigned char)(*buf++))] ^ ((crc) << 8))

    return crc;
}
```



A code snippet used to compute the crc table is provided below:

```
/* the CRC polynomial. */
#define P 0x1021

/* number of bits in CRC: don't change it. */
#define W 16

/* this the number of bits per char: don't change it. */
#define B 8

void initcrctab(void)
{
    int b, v, i;

    for( b = 0; b <= (1<< B)-1; ++b )
    {
        for( v = b<<(W-B), i = B; --i >= 0; )
            v = v&0x8000 ? (v<<1)^P : v<<1;
        crc16tab[b] = v;
    }
}
```

3. PBus Commands

PBus follows the memory mapped paradigm. That is to say that all values that can be read or write from Remote Control Program are grouped together in a logical basis. All values that affect a specific feature and have similar needs about access (read only or read/write, etc.) are grouped in a pbus **area**.

An application that run PBus protocol must defines an address space containing a number of areas that can be addressed by pbus command according to access constrains defined for the single areas.

The addresses are 32 bit wide.

The PBus command represent a generic operation. The operation is defined at least by an address (to refer into a defined area in the address space) and a length (to know the extent of the operation).

The PBus command encapsulated in both request and reply frame have the same structure:

OPCODE	ADDR32	SIZE32	DATA
1 byte	4 bytes	4 bytes	variable

- **OPCODE** is command (operation) code.
- **ADDR32** is address field as previously described. It represent "where" to apply command.
- **SIZE32** is the length (extent) of the operation.
- **DATA** is an optional field depending on the operation. It contain the data stream involved in the operation.

PBus requests with LEN 0 are forbidden; The LEN 0 is reserved in replies to indicate a **NAK**. For example suppose the equipment defines an area from 0x01030000 to 0x01030100; a PBus command can request an operation at address 0x01030050 and length 10; for that request a reply will take place. Instead if the request is at address 0x010300F0 and length is 100 the reply will be a NAK (in other words a pbus command reply with len 0).

In general a request defines an area (address + len) where an operation must be performed. This request area must be inclusive in a defined area, otherwise a NAK reply will be generated

3.1. Pbus Read Command 'R' (0x52)

PBus command for the read operation is:

Field	Offset	Size	Description
OPCODE	0	1	'R' (0x52)
ADDR32	1	4	Address where to start reading.
SIZE32	5	4	Length of data involved in the read

Reply for read command is:

Field	Offset	Size	Description
OPCODE	0	1	'R' (0x52)
ADDR32	1	4	The address where reading has take place. Is the same value of issue command
SIZE32	5	4	Size of data stream involved in the read operation
DATA	9	SIZE32	Data stream read back at address ADDR32 with length SIZE32.

Note: if the operation cannot be performed reply will have SIZE32 0 and no DATA field. This is NAK condition.

Replies with SIZE32 different from issue command are not allowed.

Example of read command:

STX	2143A3FF	R	00030100	00000004	CRC16	ETX
-----	----------	---	----------	----------	-------	-----

Example of reply:

STX	4D5A4F	0001	2143A3FF	R	00030100	00000004	76E31003	CRC16	ETX
-----	--------	------	----------	---	----------	----------	----------	-------	-----

Example of NAK reply:

STX	4D5A4F	0001	2143A3FF	R	00030100	00000000	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

3.2. Pbus Write Command 'W' (0x57)

PBus command for the write operation is:

Field	Offset	Size	Description
OPCODE	0	1	'W' (0x57)
ADDR32	1	4	Address where to start writing.
SIZE32	5	4	Length of data involved in the write
DATA	9	SIZE32	Data stream to be wrote at address ADDR32 with length SIZE32.

Reply for write command is:

Field	Offset	Size	Description
OPCODE	0	1	'W' (0x57)
ADDR32	1	4	The address where writing has take place. Is the same value of issue command
SIZE32	5	4	Size of data stream involved in the write operation

Note: if the operation cannot be performed reply will have SIZE32 0. This is NAK condition.

Replies with SIZE32 different from issued command are not allowed.

Example of write command:

STX	23D47F30	W	B4022A00	00000001	3F	CRC16	ETX
-----	----------	---	----------	----------	----	-------	-----

Example of reply:

STX	4D5A4F	0001	23D47F30	W	B4022A00	00000001	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

Example of NAK reply:

STX	4D5A4F	0001	23D47F30	W	B4022A00	00000000	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

3.3. Pbus Erase Command 'E' (0x45)

PBus command for the erase operation is:

Field	Offset	Size	Description
OPCODE	0	1	'E' (0x45)
ADDR32	1	4	Address where to start erasing.
SIZE32	5	4	Length of data to be erased.

Reply for write command is:

Field	Offset	Size	Description
OPCODE	0	1	'E' (0x45)
ADDR32	1	4	The address where writing has take place. Is the same value of issue command
SIZE32	5	4	Size of data stream involved in the write operation

Note: if the operation cannot be performed reply will have SIZE32 0. This is NAK condition.

Replies with SIZE32 different from issued command are not allowed.

Example of erase command:

STX	930045F4	E	300FA030	00000120	CRC16	ETX
-----	----------	---	----------	----------	-------	-----

Example of reply:

STX	4D5A4F	0001	930045F4	E	300FA030	00000120	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

Example of NAK reply:

STX	4D5A4F	0001	930045F4	E	300FA030	00000000	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

3.4. Pbus Crc Command 'C' (0x43)

PBus command for the crc operation is:

Field	Offset	Size	Description
OPCODE	0	1	'C' (0x43)
ADDR32	1	4	Address where to start CRC computing.
SIZE32	5	4	Length of data involved in computation of CRC

Reply for write command is:

Field	Offset	Size	Description
OPCODE	0	1	'C' (0x43)
ADDR32	1	4	The address where writing has take place. Is the same value of issue command
SIZE32	5	4	Size of data stream involved in the write operation
DATA (CRC16)	9	2	CRC16 computed on data at address ADDR32 and length SIZE32. It is expressed in little endian.

Note: if the operation cannot be performed reply will have SIZE32 0. This is NAK condition.

Replies with SIZE32 different from issued command are not allowed.

Example of crc command:

STX	A2349AB4	C	00AF1000	00000400	CRC16	ETX
-----	----------	---	----------	----------	-------	-----

Example of reply:

STX	4D5A4F	0001	A2349AB4	C	00AF1000	00000400	C13A	CRC16	ETX
-----	--------	------	----------	---	----------	----------	------	-------	-----

Example of NAK reply:

STX	4D5A4F	0001	A2349AB4	C	00AF1000	00000000	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

3.5. Protocol Frame Examples

For the subsequent examples suppose that equipment defines 3 only areas in his PBus address space:

Unused	Area 1 (r-c) 0x00001000 ~ 0x00002000	Area 2 (rw-) 0x00002000 ~ 0x00002010	Unused	Area 3 (rwec) 0x00002300 ~ 0x00003000	Unused
---------------	---	---	---------------	--	---------------

Area 1 and Area 2 are adjoined, and all areas have different access right, that is to say that is forbidden to write in Area 1, or erase Area 2. Area 3 has full access rights.

The forthcoming examples shows some erroneous cases.

3.5.1. Example: operation not permitted

request

STX	764234A3	W	00001010	00000004	01234567	CRC16	ETX
-----	----------	---	----------	----------	----------	-------	-----

This request involves Area 1 (address 0x00001010) and generates a NAK reply because the write operation cannot be performed on Area 1; only read and crc operation are permitted for this area.

3.5.2. Example: operation across two areas

STX	5467A4CF	R	00002000	00000400	CRC16	ETX
-----	----------	---	----------	----------	-------	-----

The operation requested begins in Area 2 (0x00002000) but would finish in Area 3 (from 0x00002300 on).

Unused	Area 1 (r-c) 0x00001000 ~ 0x00002000	Area 2 (rw-) 0x00002000 ~ 0x00002010	Unused	Area 3 (rwec) 0x00002300 ~ 0x00003000	Unused
Read operation requested 0x00002000 ~ 0x000023FF					

This is not allowed and generates a NAK reply. No matter if the request operation is permitted for all areas involved. Only one area can be involved in an operation.

3.5.3. Example: another operation across two areas

Request:

STX	AA3426C3	R	00001FF0	00000200	CRC16	ETX
-----	----------	---	----------	----------	-------	-----

The operation requested begins in Area 1 (0x00001FF0) but would finish in Area 2 (from 0x00002000 to 0x0000200F).

Unused	Area 1 (r-c) 0x00001000 ~ 0x00002000	Area 2 (rw-) 0x00002000 ~ 0x00002010	Unused	Area 3 (rwec) 0x00002300 ~ 0x00003000	Unused
Read operation requested 0x00002000 ~ 0x000023FF					

This is not allowed and generates a NAK reply. No matter if the request operation involve contiguous areas. Only one area can be involved in an operation.

3.5.4. Example: operation on unallocated address space

Request:

STX	34453D3C	C	00004000	00000180	CRC16	ETX
-----	----------	---	----------	----------	-------	-----

The address 0x04004000 is unallocated space. No areas are defined there, so this request generates a NAK reply.

3.5.5. Example: unknown operation

Request:

STX	C1B8C3B0	Q	00001000	00000004	12A467A5	CRC16	ETX
-----	----------	---	----------	----------	----------	-------	-----

The OPCODE is none of known operation ('R', 'W', 'E' or 'C'). It could be not possible to parse that frame (for example, we cannot know if data field is present or not, we know only for known opcode). We must consider it as a corrupted frame and no replies will be generated.

3.5.6. Example: malformed frame

Request:

STX	DA7A3498	C	00001100	00000004	12A467A5	CRC16	ETX
-----	----------	---	----------	----------	----------	-------	-----

The PBus command for this opcode doesn't admit presence of DATA field¹. This frame cannot be validate. We must consider it as a corrupted frame and no replies will be generated.

3.5.7. Example: another malformed frame

Request:

STX	62378A34	W	00001A00	00000010	010203	CRC16	ETX
-----	----------	---	----------	----------	--------	-------	-----

The request involves Area 2 that support write operation, but DATA field is shorter than expected². Only 3 bytes are present instead of 16 bytes declared in SIZE32 field. We must consider it as a corrupted frame and no replies will be generated.

3.5.8. Example: CRC16 error

Request:

STX	78932A34	E	00002400	00000140	xxxx	ETX
-----	----------	---	----------	----------	------	-----

Crc computed on this frame doesn't match the value in CRC16 field. We must consider it as a corrupted frame and no replies will be generated.

-
1. Parser cannot make any assumption on the presence of DATA field during parsing since there is no special character to bound a PBus command. It must assume that no DATA is present so first byte of DATA field will be treat as OPCODE of a subsequent PBus command. Length control will invalidate this frame.
 2. Parser must assume that SIZE32 bytes are present in DATA field. Length control will invalidate this frame.
-

3.6. Protocol frame example with multicommand

The forthcoming examples shows some frame with multicommand with more emphasis on erroneous cases. Consider the preceding address space mapping.

3.6.1. Example: correct multicommand

Request

STX	DF3452B2	PBus command 1	PBus command 2	PBus command 3	CRC16	ETX
R	00001020	00000002 W	00002000	00000002 37A2 E	00002400	00000100

This request involves all defined areas. All command are correct and the reply will be:

STX	4D5A4F 0001	DF3452B2	PBus cmd reply 1	PBus cmd reply 2	PBus cmd reply 3	CRC16	ETX
R	00001020	00000002	4455 W	00002000	00000002 E	00002400	00000100

The commands are executed sequentially from left to right (from command 1 to command n). Note that is possible to request more operation on the same area (same addresses) in a single multicommand.

3.6.2. Example: multicommand with error

Request

STX	7643D4F4	PBus command 1	PBus command 2	PBus command 3	CRC16	ETX
C	00001100	00000100 C	00002000	00000010 E	00002400	00000100

Reply

STX	4D5A4F 0001	7643D4F4	PBus cmd reply 1	PBus cmd reply 2	PBus cmd reply 3	CRC16	ETX
C	00001100	00000100	CD4F C	00002000	00000010 E	00002400	00000100

PBus command 2 request to perform a crc operation on Area 2 that do not support crc. Only PBus command reply 2 will be a NAK.

In general only failed command1 will populate the reply with NAK, no matter of the position in multicommand or outcome of other commands (if any) in multicommand.

3.6.3. Example: another multicommand with error

Request:

STX	9483A3AA	PBus command 1	PBus command 2	PBus command 3	CRC16	ETX
R	00002008	00000010 E	00002300	00000008 W	00003400	00000006 A3DE341556FF

Reply

STX	4D5A4F 0001	9483A3AA	PBus cmd reply 1	PBus cmd reply 2	PBus cmd reply 3	CRC16	ETX
R	00002008	00000000 E	00002300	00000008 W	00003400	00000000	

PBus command 1 fails due to a read operation that exceed area 2 limits. Pbus command 3 wants to write to unallocated addresses.

3.6.4. Example: further multicommand with error

Request:

STX	345344AF	PBus command 1	PBus command 2	PBus command 3	PBus command 4	CRC16	ETX
R	0000101A	00000010 E	00002300	00000008 A	00003400	00000006 A3DE C	04002500 00000D00
				↑	↑	↑	

This multicommand request cannot be parsed. PBus command 3 must be consider corrupted due to the reasons below:

- The failure is intended to be such as a NAK replies can be generated, instead of a corrupted frame request.

- **OPCODE** 'A' is unknown.

- **SIZE32** and size of DATA field doesn't match1

Anyone of the points above by itself may suffice to invalidate PBus command 3. Since it is not be feasible to parse PBus command 3, also any subsequent PBus commands cannot be parsed.

No reply is generated for this request.

3.6.5. Example: false positive

The case presented below is one that should be considered wrong, because the request is malformed or corrupted, but it cannot be possible to establish the wrongness of request. And an operation is performed by mistake.

Request:

STX	AA2435E4	PBus command 1		PBus command 2		CRC16	ETX
W	00002300	0000000C	142A36	C	00001F00	00000100	
		↑	↑		9 bytes		

This request should be considered as corrupted, but due to the circumstance it will be considered as correct frame composed by a single PBus command.

Indeed in PBus command 1 is asked a write operation on Area 3, 10 bytes length but DATA field contains only 3 bytes. The subsequent PBus command 2 takes 9 bytes (1 byte for OPCODE, 4 for ADDR32, and 4 for SIZE32). Parse cannot help but consider PBus command 2 as part of DATA field of PBus command 1, no matter if PBus command 2 is formerly and syntactically correct or not. Only its length is important.

-
4. In this case parser would consider DATA field of PBus command 3 to be 6 bytes, so it assumes that the first bytes of PBus command 4 to belong DATA field of PBus command 3. Syntax verification and length control will invalidate this frame.
-

In this case a write operation will be performed and the reply will be:

STX	4D5A4F	0001	AA2435E4	W	00002300	0000000C	CRC16	ETX
-----	--------	------	----------	---	----------	----------	-------	-----

Since every PBus command issued should generate a PBus command reply, the Remote Control Program should establish an error condition, but a mistaken operation is already performed.

Mezzo

This is the Mezzo Protocol

BlockId	Start Address	End Address	Description
Info	0x00000000	0x00000510	This area contains general informations related to the device.
Network	0x00001000	0x0000130c	This area contains network informations related to the device. It is divided in the following areas:
Source selection	0x00002000	0x000021a8	This area contains informations related to the amplifiers sources.
Matrix	0x00003000	0x00003068	This area contains informations related to the amplifiers sources.
User	0x00004000	0x00004280	This area contains informations related to the amplifiers sources.
Speaker Layout	0x00005000	0x000062f4	This area contains informations related to the output routing.
Ways	0x00007000	0x00007950	This area contains informations related to the amplifiers ways.
Dante routing	0x00008500	0x00008518	This area contains informations related to the amplifiers sources.
GPI configuration????	0x00009000	0x0000a000	This area contains informations related to the amplifiers sources.
Power config	0x0000a000	0x0000a008	This area contains informations related to the amplifiers sources.
Readings	0x0000b000	0x0000bbd0	This area contains informations related to the amplifiers sources.
AutoSetup	0x0000c000	0x0000ef04	This area contains informations related to the autoseup.
Blink	0x00100000	0x00100001	The blink command
System Reboot	0x00100001	0x00100002	The system reboot command
Firmware Area	0x00700000	0x00800000	This area contains the firmware. The max size for firmware is 1048576
Firmware Start	0x00900000	0x00900006	This area contains the new firmware information, use this to verify the firmware before flash it
Firmware Flash Erase	0x00900010	0x00900011	This will start the upgrade firmware

Info

This area contains general informations related to the device. It is divided in the following two(2) areas:

BlockId	Start Address	End Address	Description
Read only area data struct	0x00000000	0x0000007a	Readonly informations
Read write client area	0x000000f4	0x00000510	Readonly informations

Read only area data struct

Readonly informations

Offset	Name	Type	Dim	R \ W	Description														
0x00000000 (0)	Model	char[20]	20	R	String NULL terminated representing the Model ID														
0x00000014 (20)	SerialNumber	char[16]	16	R	tring NULL terminated representing the serial number														
0x00000024 (36)	ManufacturerID	char[20]	20	R	String NULL terminated representing the manufacturer identifier														
0x00000038 (56)	ManufacturerModel	char[20]	20	R	String NULL terminated representing the manufacturer model identifier														
0x0000004c (76)	ManufacturerSerialNumber	char[20]	20	R	String NULL terminated representing the manufacturer serial number														
0x00000060 (96)	FWInfo	char[20]	20	R	String NULL terminated representing the firmware version														
0x00000074 (116)	MAC Address	uint8[6]	6	R/W	Actual (running) MAC address expressed in big endian. For example if MAC address is 00:21:84:01:02:03, bytes are: <table border="1" data-bbox="970 1016 1246 1234"> <thead> <tr> <th>offset</th> <th>bytes</th> </tr> </thead> <tbody> <tr> <td>0x00</td> <td>0x00</td> </tr> <tr> <td>0x01</td> <td>0x21</td> </tr> <tr> <td>0x02</td> <td>0x84</td> </tr> <tr> <td>0x03</td> <td>0x01</td> </tr> <tr> <td>0x04</td> <td>0x02</td> </tr> <tr> <td>0x05</td> <td>0x03</td> </tr> </tbody> </table>	offset	bytes	0x00	0x00	0x01	0x21	0x02	0x84	0x03	0x01	0x04	0x02	0x05	0x03
offset	bytes																		
0x00	0x00																		
0x01	0x21																		
0x02	0x84																		
0x03	0x01																		
0x04	0x02																		
0x05	0x03																		

Read write client area

Readonly informations

BlockId	Start Address	End Address	Description
NickName	0x000000f4	0x00000144	NickName
ClientSpare	0x00000144	0x00000510	Spare area for client usage

NickName

NickName

Offset	Name	Type	Dim	R \ W	Description
0x000000f4 (244)	NickName	char[20]	80	R/W	String NULL terminated representing the device Nick name (set from the client)

ClientSpare

Spare area for client usage

BlockId	Start Address	End Address	Description
Locking	0x00000144	0x00000444	Locking parameters
Empty Client Spare	0x00000444	0x00000510	Spare area for client usage

Locking

Locking parameters

BlockId	Start Address	End Address	Description
Way 1 Eq	0x00000144	0x00000164	Way 1 eq Locking parameters
Way 2 Eq	0x00000164	0x00000184	Way 2 eq Locking parameters
Way 3 Eq	0x00000184	0x000001a4	Way 3 eq Locking parameters
Way 4 Eq	0x000001a4	0x000001c4	Way 4 eq Locking parameters
Speaker 1 Eq	0x000001c4	0x000001e4	Speaker 1 eq Locking parameters
Speaker 2 Eq	0x000001e4	0x00000204	Speaker 2 eq Locking parameters
Speaker 3 Eq	0x00000204	0x00000224	Speaker 3 eq Locking parameters
Speaker 4 Eq	0x00000224	0x00000244	Speaker 4 eq Locking parameters
Way 1 Damping	0x00000244	0x00000264	Way 1 Damping Locking parameters
Way 2 Damping	0x00000264	0x00000284	Way 2 Damping Locking parameters
Way 3 Damping	0x00000284	0x000002a4	Way 3 Damping Locking parameters
Way 4 Damping	0x000002a4	0x000002c4	Way 4 Damping Locking parameters
Way 1 RMS limiter	0x000002c4	0x000002e4	Way 1 RMS limiter Locking parameters
Way 2 RMS limiter	0x000002e4	0x00000304	Way 2 RMS limiter Locking parameters
Way 3 RMS limiter	0x00000304	0x00000324	Way 3 RMS limiter Locking parameters
Way 4 RMS limiter	0x00000324	0x00000344	Way 4 RMS limiter Locking parameters
Way 1 Peak limiter	0x00000344	0x00000364	Way 1 Peak limiter Locking parameters
Way 2 Peak limiter	0x00000364	0x00000384	Way 2 Peak limiter Locking parameters
Way 3 Peak limiter	0x00000384	0x000003a4	Way 3 Peak limiter Locking parameters
Way 4 Peak limiter	0x000003a4	0x000003c4	Way 4 Peak limiter Locking parameters
Way 1 Clip limiter	0x000003c4	0x000003e4	Way 1 Clip limiter Locking parameters
Way 2 Clip limiter	0x000003e4	0x00000404	Way 2 Clip limiter Locking parameters
Way 3 Clip limiter	0x00000404	0x00000424	Way 3 Clip limiter Locking parameters
Way 4 Clip limiter	0x00000424	0x00000444	Way 4 Clip limiter Locking parameters

Way 1 Eq

Way 1 eq Locking parameters

Offset	Name	Type	Dim	R W	Description
0x00000144 (324)	Mode	uint32	4	RW	The locking mode
0x00000148 (328)	Password	char[28]	28	RW	The locking password

Way 2 Eq

Way 2 eq Locking parameters

Offset	Name	Type	Dim	R W	Description
0x00000164 (356)	Mode	uint32	4	RW	The locking mode
0x00000168 (360)	Password	char[28]	28	RW	The locking password

Way 3 Eq

Way 3 eq Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000184 (388)	Mode	uint32	4	RW	The locking mode
0x00000188 (392)	Password	char[28]	28	RW	The locking password

Way 4 Eq

Way 4 eq Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000001a4 (420)	Mode	uint32	4	RW	The locking mode
0x000001a8 (424)	Password	char[28]	28	RW	The locking password

Speaker 1 Eq

Speaker 1 eq Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000001c4 (452)	Mode	uint32	4	RW	The locking mode
0x000001c8 (456)	Password	char[28]	28	RW	The locking password

Speaker 2 Eq

Speaker 2 eq Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000001e4 (484)	Mode	uint32	4	RW	The locking mode
0x000001e8 (488)	Password	char[28]	28	RW	The locking password

Speaker 3 Eq

Speaker 3 eq Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000204 (516)	Mode	uint32	4	RW	The locking mode
0x00000208 (520)	Password	char[28]	28	RW	The locking password

Speaker 4 Eq

Speaker 4 eq Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000224 (548)	Mode	uint32	4	RW	The locking mode
0x00000228 (552)	Password	char[28]	28	RW	The locking password

Way 1 Damping

Way 1 Damping Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000244 (580)	Mode	uint32	4	RW	The locking mode
0x00000248 (584)	Password	char[28]	28	RW	The locking password

Way 2 Damping

Way 2 Damping Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000264 (612)	Mode	uint32	4	RW	The locking mode
0x00000268 (616)	Password	char[28]	28	RW	The locking password

Way 3 Damping

Way 3 Damping Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000284 (644)	Mode	uint32	4	RW	The locking mode
0x00000288 (648)	Password	char[28]	28	RW	The locking password

Way 4 Damping

Way 4 Damping Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000002a4 (676)	Mode	uint32	4	RW	The locking mode
0x000002a8 (680)	Password	char[28]	28	RW	The locking password

Way 1 RMS limiter

Way 1 RMS limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000002c4 (708)	Mode	uint32	4	RW	The locking mode
0x000002c8 (712)	Password	char[28]	28	RW	The locking password

Way 2 RMS limiter

Way 2 RMS limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000002e4 (740)	Mode	uint32	4	RW	The locking mode
0x000002e8 (744)	Password	char[28]	28	RW	The locking password

Way 3 RMS limiter

Way 3 RMS limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000304 (772)	Mode	uint32	4	RW	The locking mode
0x00000308 (776)	Password	char[28]	28	RW	The locking password

Way 4 RMS limiter

Way 4 RMS limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000324 (804)	Mode	uint32	4	RW	The locking mode
0x00000328 (808)	Password	char[28]	28	RW	The locking password

Way 1 Peak limiter

Way 1 Peak limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000344 (836)	Mode	uint32	4	RW	The locking mode
0x00000348 (840)	Password	char[28]	28	RW	The locking password

Way 2 Peak limiter

Way 2 Peak limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000364 (868)	Mode	uint32	4	RW	The locking mode
0x00000368 (872)	Password	char[28]	28	RW	The locking password

Way 3 Peak limiter

Way 3 Peak limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000384 (900)	Mode	uint32	4	RW	The locking mode
0x00000388 (904)	Password	char[28]	28	RW	The locking password

Way 4 Peak limiter

Way 4 Peak limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000003a4 (932)	Mode	uint32	4	RW	The locking mode
0x000003a8 (936)	Password	char[28]	28	RW	The locking password

Way 1 Clip limiter

Way 1 Clip limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000003c4 (964)	Mode	uint32	4	RW	The locking mode
0x000003c8 (968)	Password	char[28]	28	RW	The locking password

Way 2 Clip limiter

Way 2 Clip limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x000003e4 (996)	Mode	uint32	4	RW	The locking mode
0x000003e8 (1000)	Password	char[28]	28	RW	The locking password

Way 3 Clip limiter

Way 3 Clip limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000404 (1028)	Mode	uint32	4	RW	The locking mode
0x00000408 (1032)	Password	char[28]	28	RW	The locking password

Way 4 Clip limiter

Way 4 Clip limiter Locking parameters

Offset	Name	Type	Dim	R \ W	Description
0x00000424 (1060)	Mode	uint32	4	RW	The locking mode
0x00000428 (1064)	Password	char[28]	28	RW	The locking password

Empty Client Spare

Spare area for client usage

Offset	Name	Type	Dim	R \ W	Description
0x00000444 (1092)	Empty Client Spare	char[200]	200	RW	Spare area for client usage

Network

This area contains network informations related to the device. It is divided in the following areas:

BlockId	Start Address	End Address	Description
NetworkSettings	0x00001000	0x0000100d	Network Settings
NetworkInformations	0x00001300	0x0000130c	Read only area containing network informations

NetworkSettings

Network Settings

BlockId	Start Address	End Address	Description
Network configuration data struct	0x00001000	0x0000100d	Network configurations

Network configuration data struct

Network configurations

Offset	Name	Type	Dim	R \ W	Description										
0x00001000 (4096)	AddressMode	uint8	1	R\W	If 1 set the equipment to use a static IP address. In the case Ip address and netmask are set by the appropriate fiend. If equal to 0, the equipment is set to dynamic address, using DHCP (or IAuto-IP)										
0x00001001 (4097)	IPAddress	uint8[4]	4	R\W	Is the IP address. This field is used only if address mode is 0 (static IP). Is expressed in big endian. <table border="1" data-bbox="906 607 1184 763"> <thead> <tr> <th>offset</th> <th>bytes</th> </tr> </thead> <tbody> <tr> <td>0x00</td> <td>0xC0 (192)</td> </tr> <tr> <td>0x01</td> <td>0xA8 (168)</td> </tr> <tr> <td>0x02</td> <td>0x37 (55)</td> </tr> <tr> <td>0x03</td> <td>0x6C (108)</td> </tr> </tbody> </table>	offset	bytes	0x00	0xC0 (192)	0x01	0xA8 (168)	0x02	0x37 (55)	0x03	0x6C (108)
offset	bytes														
0x00	0xC0 (192)														
0x01	0xA8 (168)														
0x02	0x37 (55)														
0x03	0x6C (108)														
0x00001005 (4101)	NetMask	uint8[4]	4	R\W	Is the NetMask address. This field is used only if address mode is 0 (static IP). Is expressed in big endian. <table border="1" data-bbox="906 891 1184 1048"> <thead> <tr> <th>offset</th> <th>bytes</th> </tr> </thead> <tbody> <tr> <td>0x00</td> <td>0xC0 (192)</td> </tr> <tr> <td>0x01</td> <td>0xA8 (168)</td> </tr> <tr> <td>0x02</td> <td>0x37 (55)</td> </tr> <tr> <td>0x03</td> <td>0x6C (108)</td> </tr> </tbody> </table>	offset	bytes	0x00	0xC0 (192)	0x01	0xA8 (168)	0x02	0x37 (55)	0x03	0x6C (108)
offset	bytes														
0x00	0xC0 (192)														
0x01	0xA8 (168)														
0x02	0x37 (55)														
0x03	0x6C (108)														
0x00001009 (4105)	DefaultGW	uint8[4]	4	R\W	Is the default gateway address. This field is used only if address mode is 0 (static IP). Is expressed in big endian.										

NetworkInformations

Read only area containing network informations

Offset	Name	Type	Dim	R \ W	Description
0x00001300 (4864)	IPAddress	uint8[4]	4	R\W	Is the IP address. Is expressed in big endian.
0x00001304 (4868)	NetMask	uint8[4]	4	R\W	Is the NetMask address. Is expressed in big endian.
0x00001308 (4872)	DefaultGW	uint8[4]	4	R\W	Is the default gateway address. Is expressed in big endian.

Source selection

This area contains informations related to the amplifiers sources.

BlockId	Start Address	End Address	Description
Amplifier input references	0x00002000	0x00002010	
Sources	0x00002100	0x000021a8	

Amplifier input references

Offset	Name	Type	Dim	R \ W	Description
0x00002000 (8192)	Analog Ref	Float	4	RW	Analog source reference in linear
0x00002004 (8196)	Analog Delay	Float	4	RW	Analog delay in s
0x00002008 (8200)	Digital Ref	Float	4	RW	Digital source reference in linear
0x0000200c (8204)	Digital Delay	Float	4	RW	Digital delay in s

Sources

BlockId	Start Address	End Address	Description
Priority1_Type	0x00002100	0x00002108	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
Priority1_Channel	0x00002108	0x00002110	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
Priority2_Type	0x00002110	0x00002118	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
Priority2_Channel	0x00002118	0x00002120	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
BackupStrategyEnable	0x00002120	0x00002124	if set to 0 backup strategy is disabled, if set to 1 is enabled
PilotToneEnable	0x00002124	0x00002128	if set to 0 pilot tone is disabled, if set to 1 is enabled
Backup threshold mode	0x00002128	0x00002138	Enum representing the backup threshold mode to be used. For the allowed values for this field refer to ...(TODO)
User digital threshold	0x00002138	0x00002148	User defined threshold for digital signal in linear
User analog threshold	0x00002148	0x00002158	User defined threshold for analog signal in linear
Pilot tone freq	0x00002158	0x00002168	Pilot tone frequency in Hz
Pilot tone high threshold	0x00002168	0x00002178	Pilot tone high threshold in Hz
Pilot tone low threshold	0x00002178	0x00002188	Pilot tone low threshold
External trigger	0x00002188	0x00002198	Enum representing the External trigger to activate
External input	0x00002198	0x000021a8	Enum representing the GPI input to be used to select the source. For the GPI Input configuration see TODO

Priority1 Type

Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)

Offset	Name	Type	Dim	R \ W	Description
0x00002100 (8448)	Priority1 Type CH 1	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x00002102 (8450)	Priority1 Type CH 2	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x00002104 (8452)	Priority1 Type CH 3	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x00002106 (8454)	Priority1 Type CH 4	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)

Priority1 Channel

Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)

Offset	Name	Type	Dim	R \ W	Description
0x00002108 (8456)	Priority1 Channel CH 1	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x0000210a (8458)	Priority1 Channel CH 2	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x0000210c (8460)	Priority1 Channel CH 3	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x0000210e (8462)	Priority1 Channel CH 4	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)

Priority2 Type

Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)

Offset	Name	Type	Dim	R \ W	Description
0x00002110 (8464)	Priority2 Type CH 1	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x00002112 (8466)	Priority2 Type CH 2	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x00002114 (8468)	Priority2 Type CH 3	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)
0x00002116 (8470)	Priority2 Type CH 4	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ... (TODO)

Priority2 Channel

Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)

Offset	Name	Type	Dim	R \ W	Description
0x00002118 (8472)	Priority2 Channel CH 1	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
0x0000211a (8474)	Priority2 Channel CH 2	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
0x0000211c (8476)	Priority2 Channel CH 3	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)
0x0000211e (8478)	Priority2 Channel CH 4	Uint16	2	RW	Enum representing the first priority for the selected source. For the allowed values for this field refer to ...(TODO)

BackupStrategyEnable

if set to 0 backup strategy is disabled, if set to 1 is enabled

Offset	Name	Type	Dim	R \ W	Description
0x00002120 (8480)	BackupStrategyEnable CH 1	uint8	1	RW	if set to 0 backup strategy is disabled, if set to 1 is enabled
0x00002121 (8481)	BackupStrategyEnable CH 2	uint8	1	RW	if set to 0 backup strategy is disabled, if set to 1 is enabled
0x00002122 (8482)	BackupStrategyEnable CH 3	uint8	1	RW	if set to 0 backup strategy is disabled, if set to 1 is enabled
0x00002123 (8483)	BackupStrategyEnable CH 4	uint8	1	RW	if set to 0 backup strategy is disabled, if set to 1 is enabled

PilotToneEnable

if set to 0 pilot tone is disabled, if set to 1 is enabled

Offset	Name	Type	Dim	R \ W	Description
0x00002124 (8484)	PilotToneEnable CH 1	uint8	1	RW	if set to 0 pilot tone is disabled, if set to 1 is enabled
0x00002125 (8485)	PilotToneEnable CH 2	uint8	1	RW	if set to 0 pilot tone is disabled, if set to 1 is enabled
0x00002126 (8486)	PilotToneEnable CH 3	uint8	1	RW	if set to 0 pilot tone is disabled, if set to 1 is enabled
0x00002127 (8487)	PilotToneEnable CH 4	uint8	1	RW	if set to 0 pilot tone is disabled, if set to 1 is enabled

Backup threshold mode

Enum representing the backup threshold mode to be used. For the allowed values for this field refer to ...(TODO)

Offset	Name	Type	Dim	R \ W	Description
0x00002128 (8488)	Backup threshold mode CH 1	Uint32	4	RW	Enum representing the backup threshold mode to be used. For the allowed values for this field refer to ...(TODO)
0x0000212c (8492)	Backup threshold mode CH 2	Uint32	4	RW	Enum representing the backup threshold mode to be used. For the allowed values for this field refer to ...(TODO)
0x00002130 (8496)	Backup threshold mode CH 3	Uint32	4	RW	Enum representing the backup threshold mode to be used. For the allowed values for this field refer to ...(TODO)
0x00002134 (8500)	Backup threshold mode CH 4	Uint32	4	RW	Enum representing the backup threshold mode to be used. For the allowed values for this field refer to ...(TODO)

User digital threshold

User defined threshold for digital signal in linear

Offset	Name	Type	Dim	R \ W	Description
0x00002138 (8504)	User digital threshold CH 1	Float	4	RW	User defined threshold for digital signal in linear
0x0000213c (8508)	User digital threshold CH 2	Float	4	RW	User defined threshold for digital signal in linear
0x00002140 (8512)	User digital threshold CH 3	Float	4	RW	User defined threshold for digital signal in linear
0x00002144 (8516)	User digital threshold CH 4	Float	4	RW	User defined threshold for digital signal in linear

User analog threshold

User defined threshold for analog signal in linear

Offset	Name	Type	Dim	R \ W	Description
0x00002148 (8520)	User analog threshold CH 1	Float	4	RW	User defined threshold for analog signal in linear
0x0000214c (8524)	User analog threshold CH 2	Float	4	RW	User defined threshold for analog signal in linear
0x00002150 (8528)	User analog threshold CH 3	Float	4	RW	User defined threshold for analog signal in linear
0x00002154 (8532)	User analog threshold CH 4	Float	4	RW	User defined threshold for analog signal in linear

Pilot tone freq

Pilot tone frequency in Hz

Offset	Name	Type	Dim	R \ W	Description
0x00002158 (8536)	Pilot tone freq CH 1	Float	4	R\W	Pilot tone frequency in Hz
0x0000215c (8540)	Pilot tone freq CH 2	Float	4	R\W	Pilot tone frequency in Hz
0x00002160 (8544)	Pilot tone freq CH 3	Float	4	R\W	Pilot tone frequency in Hz
0x00002164 (8548)	Pilot tone freq CH 4	Float	4	R\W	Pilot tone frequency in Hz

Pilot tone high threshold

Pilot tone high threshold in Hz

Offset	Name	Type	Dim	R \ W	Description
0x00002168 (8552)	Pilot tone high threshold CH 1	Float	4	R\W	Pilot tone high threshold in Hz
0x0000216c (8556)	Pilot tone high threshold CH 2	Float	4	R\W	Pilot tone high threshold in Hz
0x00002170 (8560)	Pilot tone high threshold CH 3	Float	4	R\W	Pilot tone high threshold in Hz
0x00002174 (8564)	Pilot tone high threshold CH 4	Float	4	R\W	Pilot tone high threshold in Hz

Pilot tone low threshold

Pilot tone low threshold

Offset	Name	Type	Dim	R \ W	Description
0x00002178 (8568)	Pilot tone low threshold CH 1	Float	4	R\W	Pilot tone low threshold
0x0000217c (8572)	Pilot tone low threshold CH 2	Float	4	R\W	Pilot tone low threshold
0x00002180 (8576)	Pilot tone low threshold CH 3	Float	4	R\W	Pilot tone low threshold
0x00002184 (8580)	Pilot tone low threshold CH 4	Float	4	R\W	Pilot tone low threshold

External trigger

Enum representing the External trigger to activate

Offset	Name	Type	Dim	R \ W	Description
0x00002188 (8584)	External trigger CH 1	Uint32	4	R\W	Enum representing the External trigger to activate
0x0000218c (8588)	External trigger CH 2	Uint32	4	R\W	Enum representing the External trigger to activate
0x00002190 (8592)	External trigger CH 3	Uint32	4	R\W	Enum representing the External trigger to activate
0x00002194 (8596)	External trigger CH 4	Uint32	4	R\W	Enum representing the External trigger to activate

External input

Enum representing the GPI input to be used to select the source. For the GPI Input configuration see TODO

Offset	Name	Type	Dim	R W	Description
0x00002198 (8600)	External input CH 1	Uint32	4	RW	Enum representing the GPI input to be used to select the source. For the GPI Input configuration see TODO
0x0000219c (8604)	External input CH 2	Uint32	4	RW	Enum representing the GPI input to be used to select the source. For the GPI Input configuration see TODO
0x000021a0 (8608)	External input CH 3	Uint32	4	RW	Enum representing the GPI input to be used to select the source. For the GPI Input configuration see TODO
0x000021a4 (8612)	External input CH 4	Uint32	4	RW	Enum representing the GPI input to be used to select the source. For the GPI Input configuration see TODO

Matrix

This area contains informations related to the amplifiers sources.

Offset	Name	Type	Dim	R W	Description
0x00003000 (12288)	Type	uint32	4	RW	Represent the matrix type currently in use
0x00003004 (12292)	Source 1 pre Mute	uint8	1	RW	Pre mute to be applied to source 1
0x00003005 (12293)	Source 2 pre Mute	uint8	1	RW	Pre mute to be applied to source 2
0x00003006 (12294)	Source 3 pre Mute	uint8	1	RW	Pre mute to be applied to source 3
0x00003007 (12295)	Source 4 pre Mute	uint8	1	RW	Pre mute to be applied to source 4
0x00003008 (12296)	Source 1 pre Gain	Float	4	RW	Pre Gain to be applied to source 1
0x0000300c (12300)	Source 2 pre Gain	Float	4	RW	Pre Gain to be applied to source 2
0x00003010 (12304)	Source 3 pre Gain	Float	4	RW	Pre Gain to be applied to source 3
0x00003014 (12308)	Source 4 pre Gain	Float	4	RW	Pre Gain to be applied to source 4
0x00003018 (12312)	Source 1 Gain 1	Float	4	RW	Linear Gain related to output 1 for the Source 1
0x0000301c (12316)	Source 1 Gain 2	Float	4	RW	Linear Gain related to output 2 for the Source 1
0x00003020 (12320)	Source 1 Gain 3	Float	4	RW	Linear Gain related to output 3 for the Source 1
0x00003024 (12324)	Source 1 Gain 4	Float	4	RW	Linear Gain related to output 4 for the Source 1
0x00003028 (12328)	Source 2 Gain 1	Float	4	RW	Linear Gain related to output 1 for the Source 2
0x0000302c (12332)	Source 2 Gain 2	Float	4	RW	Linear Gain related to output 2 for the Source 2
0x00003030 (12336)	Source 2 Gain 3	Float	4	RW	Linear Gain related to output 3 for the Source 2
0x00003034 (12340)	Source 2 Gain 4	Float	4	RW	Linear Gain related to output 4 for the Source 2
0x00003038 (12344)	Source 3 Gain 1	Float	4	RW	Linear Gain related to output 1 for the Source 3
0x0000303c (12348)	Source 3 Gain 2	Float	4	RW	Linear Gain related to output 2 for the Source 3
0x00003040 (12352)	Source 3 Gain 3	Float	4	RW	Linear Gain related to output 3 for the Source 3
0x00003044 (12356)	Source 3 Gain 4	Float	4	RW	Linear Gain related to output 4 for the Source 3
0x00003048 (12360)	Source 4 Gain 1	Float	4	RW	Linear Gain related to output 1 for the Source 4
0x0000304c (12364)	Source 4 Gain 2	Float	4	RW	Linear Gain related to output 2 for the Source 4
0x00003050 (12368)	Source 4 Gain 3	Float	4	RW	Linear Gain related to output 3 for the Source 4
0x00003054 (12372)	Source 4 Gain 4	Float	4	RW	Linear Gain related to output 4 for the Source 4
0x00003058	Source 1 Mute 1	uint8	1	RW	Mute to be applied to source 1



(12376)					
0x00003059 (12377)	Source 1 Mute 2	uint8	1	RW	Mute to be applied to source 2
0x0000305a (12378)	Source 1 Mute 3	uint8	1	RW	Mute to be applied to source 3
0x0000305b (12379)	Source 1 Mute 4	uint8	1	RW	Mute to be applied to source 4
0x0000305c (12380)	Source 2 Mute 1	uint8	1	RW	Mute to be applied to source 1
0x0000305d (12381)	Source 2 Mute 2	uint8	1	RW	Mute to be applied to source 2
0x0000305e (12382)	Source 2 Mute 3	uint8	1	RW	Mute to be applied to source 3
0x0000305f (12383)	Source 2 Mute 4	uint8	1	RW	Mute to be applied to source 4
0x00003060 (12384)	Source 3 Mute 1	uint8	1	RW	Mute to be applied to source 1
0x00003061 (12385)	Source 3 Mute 2	uint8	1	RW	Mute to be applied to source 2
0x00003062 (12386)	Source 3 Mute 3	uint8	1	RW	Mute to be applied to source 3
0x00003063 (12387)	Source 3 Mute 4	uint8	1	RW	Mute to be applied to source 4
0x00003064 (12388)	Source 4 Mute 1	uint8	1	RW	Mute to be applied to source 1
0x00003065 (12389)	Source 4 Mute 2	uint8	1	RW	Mute to be applied to source 2
0x00003066 (12390)	Source 4 Mute 3	uint8	1	RW	Mute to be applied to source 3
0x00003067 (12391)	Source 4 Mute 4	uint8	1	RW	Mute to be applied to source 4

User

This area contains informations related to the amplifiers sources.

BlockId	Start Address	End Address	Description
User Common Settings	0x00004000	0x00004038	This area contains the user block common settings
User EQ	0x00004100	0x00004280	This area contains the user block common settings

User Common Settings

This area contains the user block common settings

BlockId	Start Address	End Address	Description
User Gain	0x00004000	0x00004010	The user gain in linear
User Delay	0x00004010	0x00004020	The delay shading
User Polarity	0x00004020	0x00004024	The user polarity
User Mute	0x00004024	0x00004028	The mute shading
User Shading	0x00004028	0x00004038	The user shading

User Gain

The user gain in linear

Offset	Name	Type	Dim	R \ W	Description
0x00004000 (16384)	User Gain 1	Float	4	R\W	The user gain in linear CH 1
0x00004004 (16388)	User Gain 2	Float	4	R\W	The user gain in linear CH 2
0x00004008 (16392)	User Gain 3	Float	4	R\W	The user gain in linear CH 3
0x0000400c (16396)	User Gain 4	Float	4	R\W	The user gain in linear CH 4

User Delay

The delay shading

Offset	Name	Type	Dim	R \ W	Description
0x00004010 (16400)	User Delay 1	Float	4	R\W	The user delay in seconds CH 1
0x00004014 (16404)	User Delay 2	Float	4	R\W	The user delay in seconds CH 2
0x00004018 (16408)	User Delay 3	Float	4	R\W	The user delay in seconds CH 3
0x0000401c (16412)	User Delay 4	Float	4	R\W	The user delay in seconds CH 4

User Polarity

The user polarity

Offset	Name	Type	Dim	R \ W	Description
0x00004020 (16416)	User Polarity 1	uint8	1	R\W	The user polarity CH 1
0x00004021 (16417)	User Polarity 2	uint8	1	R\W	The user polarity CH 2
0x00004022 (16418)	User Polarity 3	uint8	1	R\W	The user polarity CH 3
0x00004023 (16419)	User Polarity 4	uint8	1	R\W	The user polarity CH 4

User Mute

The mute shading

Offset	Name	Type	Dim	R \ W	Description
0x00004024 (16420)	User Mute 1	uint8	1	R\W	The user mute CH 1
0x00004025 (16421)	User Mute 2	uint8	1	R\W	The user mute CH 2
0x00004026 (16422)	User Mute 3	uint8	1	R\W	The user mute CH 3
0x00004027 (16423)	User Mute 4	uint8	1	R\W	The user mute CH 4

User Shading

The user shading

Offset	Name	Type	Dim	R \ W	Description
0x00004028 (16424)	User Shading 1	Float	4	R\W	The user shading CH 1
0x0000402c (16428)	User Shading 2	Float	4	R\W	The user shading CH 2
0x00004030 (16432)	User Shading 3	Float	4	R\W	The user shading CH 3
0x00004034 (16436)	User Shading 4	Float	4	R\W	The user shading CH 4

User EQ

This area contains the user block common settings

BlockId	Start Address	End Address	Description
User EQ Channel 1	0x00004100	0x00004160	This area contains the user block common settings
User EQ Channel 2	0x00004160	0x000041c0	This area contains the user block common settings
User EQ Channel 3	0x000041c0	0x00004220	This area contains the user block common settings
User EQ Channel 4	0x00004220	0x00004280	This area contains the user block common settings

User EQ Channel 1

This area contains the user block common settings

BlockId	Start Address	End Address	Description
User Eq Channel 1 BiQuad 1 settings	0x00004100	0x00004118	This area contains the user equalizer biQuad settings.
User Eq Channel 1 BiQuad 2 settings	0x00004118	0x00004130	This area contains the user equalizer biQuad settings.
User Eq Channel 1 BiQuad 3 settings	0x00004130	0x00004148	This area contains the user equalizer biQuad settings.
User Eq Channel 1 BiQuad 4 settings	0x00004148	0x00004160	This area contains the user equalizer biQuad settings.

User Eq Channel 1 BiQuad 1 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004100 (16640)	Enabled	uint32	4	R\W	The enable flag														
0x00004104 (16644)	Type	uint32	4	R\W	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
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1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00004108 (16648)	Q	Float	4	R\W	The filter Q														
0x0000410c (16652)	Slope	Float	4	R\W	The filter Slope														
0x00004110 (16656)	Frequency	uint32	4	R\W	The filter frequency														
0x00004114 (16660)	Gain	Float	4	R\W	The linear gain														

User Eq Channel 1 BiQuad 2 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004118 (16664)	Enabled	uint32	4	R\W	The enable flag														
0x0000411c (16668)	Type	uint32	4	R\W	The filter type. Valid values are: <table border="1" data-bbox="938 1332 1214 1552"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00004120 (16672)	Q	Float	4	R\W	The filter Q														
0x00004124 (16676)	Slope	Float	4	R\W	The filter Slope														
0x00004128 (16680)	Frequency	uint32	4	R\W	The filter frequency														
0x0000412c (16684)	Gain	Float	4	R\W	The linear gain														

User Eq Channel 1 BiQuad 3 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004130 (16688)	Enabled	uint32	4	RW	The enable flag														
0x00004134 (16692)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 474 1216 689"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
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5	0x03																		
0x00004138 (16696)	Q	Float	4	RW	The filter Q														
0x0000413c (16700)	Slope	Float	4	RW	The filter Slope														
0x00004140 (16704)	Frequency	uint32	4	RW	The filter frequency														
0x00004144 (16708)	Gain	Float	4	RW	The linear gain														

User Eq Channel 1 BiQuad 4 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004148 (16712)	Enabled	uint32	4	RW	The enable flag														
0x0000414c (16716)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1216 1545"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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0x00004150 (16720)	Q	Float	4	RW	The filter Q														
0x00004154 (16724)	Slope	Float	4	RW	The filter Slope														
0x00004158 (16728)	Frequency	uint32	4	RW	The filter frequency														
0x0000415c (16732)	Gain	Float	4	RW	The linear gain														

User EQ Channel 2

This area contains the user block common settings

BlockId	Start Address	End Address	Description
User Eq Channel 2 BiQuad 1 settings	0x00004160	0x00004178	This area contains the user equalizer biQuad settings.
User Eq Channel 2 BiQuad 2 settings	0x00004178	0x00004190	This area contains the user equalizer biQuad settings.
User Eq Channel 2 BiQuad 3 settings	0x00004190	0x000041a8	This area contains the user equalizer biQuad settings.
User Eq Channel 2 BiQuad 4 settings	0x000041a8	0x000041c0	This area contains the user equalizer biQuad settings.

User Eq Channel 2 BiQuad 1 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004160 (16736)	Enabled	uint32	4	RW	The enable flag														
0x00004164 (16740)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 969 1214 1189"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
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5	0x03																		
0x00004168 (16744)	Q	Float	4	RW	The filter Q														
0x0000416c (16748)	Slope	Float	4	RW	The filter Slope														
0x00004170 (16752)	Frequency	uint32	4	RW	The filter frequency														
0x00004174 (16756)	Gain	Float	4	RW	The linear gain														

User Eq Channel 2 BiQuad 2 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004178 (16760)	Enabled	uint32	4	RW	The enable flag														
0x0000417c (16764)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
4	0x02																		
5	0x03																		
0x00004180 (16768)	Q	Float	4	RW	The filter Q														
0x00004184 (16772)	Slope	Float	4	RW	The filter Slope														
0x00004188 (16776)	Frequency	uint32	4	RW	The filter frequency														
0x0000418c (16780)	Gain	Float	4	RW	The linear gain														

User Eq Channel 2 BiQuad 3 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004190 (16784)	Enabled	uint32	4	RW	The enable flag														
0x00004194 (16788)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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5	0x03																		
0x00004198 (16792)	Q	Float	4	RW	The filter Q														
0x0000419c (16796)	Slope	Float	4	RW	The filter Slope														
0x000041a0 (16800)	Frequency	uint32	4	RW	The filter frequency														
0x000041a4 (16804)	Gain	Float	4	RW	The linear gain														

User Eq Channel 2 BiQuad 4 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000041a8 (16808)	Enabled	uint32	4	RW	The enable flag														
0x000041ac (16812)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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5	0x03																		
0x000041b0 (16816)	Q	Float	4	RW	The filter Q														
0x000041b4 (16820)	Slope	Float	4	RW	The filter Slope														
0x000041b8 (16824)	Frequency	uint32	4	RW	The filter frequency														
0x000041bc (16828)	Gain	Float	4	RW	The linear gain														

User EQ Channel 3

This area contains the user block common settings

BlockId	Start Address	End Address	Description
User Eq Channel 3 BiQuad 1 settings	0x000041c0	0x000041d8	This area contains the user equalizer biQuad settings.
User Eq Channel 3 BiQuad 2 settings	0x000041d8	0x000041f0	This area contains the user equalizer biQuad settings.
User Eq Channel 3 BiQuad 3 settings	0x000041f0	0x00004208	This area contains the user equalizer biQuad settings.
User Eq Channel 3 BiQuad 4 settings	0x00004208	0x00004220	This area contains the user equalizer biQuad settings.

User Eq Channel 3 BiQuad 1 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000041c0 (16832)	Enabled	uint32	4	RW	The enable flag														
0x000041c4 (16836)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x000041c8 (16840)	Q	Float	4	RW	The filter Q														
0x000041cc (16844)	Slope	Float	4	RW	The filter Slope														
0x000041d0 (16848)	Frequency	uint32	4	RW	The filter frequency														
0x000041d4 (16852)	Gain	Float	4	RW	The linear gain														

User Eq Channel 3 BiQuad 2 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000041d8 (16856)	Enabled	uint32	4	RW	The enable flag														
0x000041dc (16860)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
4	0x02																		
5	0x03																		
0x000041e0 (16864)	Q	Float	4	RW	The filter Q														
0x000041e4 (16868)	Slope	Float	4	RW	The filter Slope														
0x000041e8 (16872)	Frequency	uint32	4	RW	The filter frequency														
0x000041ec (16876)	Gain	Float	4	RW	The linear gain														

User Eq Channel 3 BiQuad 3 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000041f0 (16880)	Enabled	uint32	4	R\W	The enable flag														
0x000041f4 (16884)	Type	uint32	4	R\W	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x000041f8 (16888)	Q	Float	4	R\W	The filter Q														
0x000041fc (16892)	Slope	Float	4	R\W	The filter Slope														
0x00004200 (16896)	Frequency	uint32	4	R\W	The filter frequency														
0x00004204 (16900)	Gain	Float	4	R\W	The linear gain														

User Eq Channel 3 BiQuad 4 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004208 (16904)	Enabled	uint32	4	R\W	The enable flag														
0x0000420c (16908)	Type	uint32	4	R\W	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
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1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00004210 (16912)	Q	Float	4	R\W	The filter Q														
0x00004214 (16916)	Slope	Float	4	R\W	The filter Slope														
0x00004218 (16920)	Frequency	uint32	4	R\W	The filter frequency														
0x0000421c (16924)	Gain	Float	4	R\W	The linear gain														

User EQ Channel 4

This area contains the user block common settings

BlockId	Start Address	End Address	Description
User Eq Channel 4 BiQuad 1 settings	0x00004220	0x00004238	This area contains the user equalizer biQuad settings.
User Eq Channel 4 BiQuad 2 settings	0x00004238	0x00004250	This area contains the user equalizer biQuad settings.
User Eq Channel 4 BiQuad 3 settings	0x00004250	0x00004268	This area contains the user equalizer biQuad settings.
User Eq Channel 4 BiQuad 4 settings	0x00004268	0x00004280	This area contains the user equalizer biQuad settings.

User Eq Channel 4 BiQuad 1 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004220 (16928)	Enabled	uint32	4	RW	The enable flag														
0x00004224 (16932)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 969 1214 1189"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
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1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00004228 (16936)	Q	Float	4	RW	The filter Q														
0x0000422c (16940)	Slope	Float	4	RW	The filter Slope														
0x00004230 (16944)	Frequency	uint32	4	RW	The filter frequency														
0x00004234 (16948)	Gain	Float	4	RW	The linear gain														

User Eq Channel 4 BiQuad 2 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004238 (16952)	Enabled	uint32	4	RW	The enable flag														
0x0000423c (16956)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 477 1214 694"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
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5	0x03																		
0x00004240 (16960)	Q	Float	4	RW	The filter Q														
0x00004244 (16964)	Slope	Float	4	RW	The filter Slope														
0x00004248 (16968)	Frequency	uint32	4	RW	The filter frequency														
0x0000424c (16972)	Gain	Float	4	RW	The linear gain														

User Eq Channel 4 BiQuad 3 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004250 (16976)	Enabled	uint32	4	RW	The enable flag														
0x00004254 (16980)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 1330 1214 1547"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00004258 (16984)	Q	Float	4	RW	The filter Q														
0x0000425c (16988)	Slope	Float	4	RW	The filter Slope														
0x00004260 (16992)	Frequency	uint32	4	RW	The filter frequency														
0x00004264 (16996)	Gain	Float	4	RW	The linear gain														

User Eq Channel 4 BiQuad 4 settings

This area contains the user equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00004268 (17000)	Enabled	uint32	4	RW	The enable flag														
0x0000426c (17004)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 474 1216 689"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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0	Peaking																		
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2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00004270 (17008)	Q	Float	4	RW	The filter Q														
0x00004274 (17012)	Slope	Float	4	RW	The filter Slope														
0x00004278 (17016)	Frequency	uint32	4	RW	The filter frequency														
0x0000427c (17020)	Gain	Float	4	RW	The linear gain														

Speaker Layout

This area contains informations related to the output routing.

BlockId	Start Address	End Address	Description
Routing Configuration	0x00005000	0x00005004	This area contains informations related to the output routing.
Speaker Settings	0x00005004	0x000062f4	This area contains informations related to the output routing.

Routing Configuration

This area contains informations related to the output routing.

Offset	Name	Type	Dim	R \ W	Description
0x00005000 (20480)	Routing	uint8[4]	4	RW	byte[4] [0,1,2,3] -> means 4 different speakers (66051 uint)

Speaker Settings

This area contains informations related to the output routing.

BlockId	Start Address	End Address	Description
Speaker Description	0x00005004	0x00005644	Speaker Description
Brand Name	0x00005644	0x000057d4	Brand name
Family Name	0x000057d4	0x00005964	Family Name
Model Name	0x00005964	0x00005af4	Model Name
Application Name	0x00005af4	0x00005c84	Application Name
Speaker Note	0x00005c84	0x000062c4	Speaker Note
Speaker Type	0x000062c4	0x000062d4	Speaker Type
Preset Type	0x000062d4	0x000062e4	Preset Type
Is HiZ Active	0x000062e4	0x000062f4	Is HiZ Active

Speaker Description

Speaker Description

Offset	Name	Type	Dim	R \ W	Description
0x00005004 (20484)	Speaker Description Speaker 1	char[400]	400	R/W	Speaker Description for speaker 1
0x00005194 (20884)	Speaker Description Speaker 2	char[400]	400	R/W	Speaker Description for speaker 2
0x00005324 (21284)	Speaker Description Speaker 3	char[400]	400	R/W	Speaker Description for speaker 3
0x000054b4 (21684)	Speaker Description Speaker 4	char[400]	400	R/W	Speaker Description for speaker 4

Brand Name

Brand name

Offset	Name	Type	Dim	R \ W	Description
0x00005644 (22084)	Brand Name Speaker 1	char[100]	100	R/W	Brand name for speaker 1
0x000056a8 (22184)	Brand Name Speaker 2	char[100]	100	R/W	Brand name for speaker 2
0x0000570c (22284)	Brand Name Speaker 3	char[100]	100	R/W	Brand name for speaker 3
0x00005770 (22384)	Brand Name Speaker 4	char[100]	100	R/W	Brand name for speaker 4

Family Name

Family Name

Offset	Name	Type	Dim	R \ W	Description
0x000057d4 (22484)	Family Name Speaker 1	char[100]	100	R\W	Family name for speaker 1
0x00005838 (22584)	Family Name Speaker 2	char[100]	100	R\W	Family name for speaker 2
0x0000589c (22684)	Family Name Speaker 3	char[100]	100	R\W	Family name for speaker 3
0x00005900 (22784)	Family Name Speaker 4	char[100]	100	R\W	Family name for speaker 4

Model Name

Model Name

Offset	Name	Type	Dim	R \ W	Description
0x00005964 (22884)	Model Name Speaker 1	char[100]	100	R\W	Model Name for speaker 1
0x000059c8 (22984)	Model Name Speaker 2	char[100]	100	R\W	Model Name for speaker 2
0x00005a2c (23084)	Model Name Speaker 3	char[100]	100	R\W	Model Name for speaker 3
0x00005a90 (23184)	Model Name Speaker 4	char[100]	100	R\W	Model Name for speaker 4

Application Name

Application Name

Offset	Name	Type	Dim	R \ W	Description
0x00005af4 (23284)	Application Name Speaker 1	char[100]	100	R\W	Application Name for speaker 1
0x00005b58 (23384)	Application Name Speaker 2	char[100]	100	R\W	Application Name for speaker 2
0x00005bbc (23484)	Application Name Speaker 3	char[100]	100	R\W	Application Name for speaker 3
0x00005c20 (23584)	Application Name Speaker 4	char[100]	100	R\W	Application Name for speaker 4

Speaker Note

Speaker Note

Offset	Name	Type	Dim	R \ W	Description
0x00005c84 (23684)	Speaker Note Speaker 1	char[400]	400	R\W	Speaker Note for speaker 1
0x00005e14 (24084)	Speaker Note Speaker 2	char[400]	400	R\W	Speaker Note for speaker 2
0x00005fa4 (24484)	Speaker Note Speaker 3	char[400]	400	R\W	Speaker Note for speaker 3
0x00006134 (24884)	Speaker Note Speaker 4	char[400]	400	R\W	Speaker Note for speaker 4

Speaker Type

Speaker Type

Offset	Name	Type	Dim	R \ W	Description
0x000062c4 (25284)	Speaker Type Speaker 1	int32	4	RW	Speaker Type for speaker 1
0x000062c8 (25288)	Speaker Type Speaker 2	int32	4	RW	Speaker Type for speaker 2
0x000062cc (25292)	Speaker Type Speaker 3	int32	4	RW	Speaker Type for speaker 3
0x000062d0 (25296)	Speaker Type Speaker 4	int32	4	RW	Speaker Type for speaker 4

Preset Type

Preset Type

Offset	Name	Type	Dim	R \ W	Description
0x000062d4 (25300)	Preset Type Speaker 1	int32	4	RW	Preset Type for speaker 1
0x000062d8 (25304)	Preset Type Speaker 2	int32	4	RW	Preset Type for speaker 2
0x000062dc (25308)	Preset Type Speaker 3	int32	4	RW	Preset Type for speaker 3
0x000062e0 (25312)	Preset Type Speaker 4	int32	4	RW	Preset Type for speaker 4

Is HiZ Active

Is HiZ Active

Offset	Name	Type	Dim	R \ W	Description
0x000062e4 (25316)	Is HiZ Active Speaker 1	uint32	4	RW	Is HiZ Active for speaker 1
0x000062e8 (25320)	Is HiZ Active Speaker 2	uint32	4	RW	Is HiZ Active for speaker 2
0x000062ec (25324)	Is HiZ Active Speaker 3	uint32	4	RW	Is HiZ Active for speaker 3
0x000062f0 (25328)	Is HiZ Active Speaker 4	uint32	4	RW	Is HiZ Active for speaker 4

Ways

This area contains informations related to the amplifiers ways.

BlockId	Start Address	End Address	Description
Way Common settings	0x00007000	0x000070a0	This area contains the way common settings.
Way Eq	0x00007200	0x00007460	This area contains the way equalizer.
Way Limiters	0x00007700	0x000077c0	This area contains the way limiters.
Way Damping Control	0x00007850	0x00007890	This area contains the way damping control settings.
Diagnostic	0x00007890	0x00007940	Ways Diagnostic area
Auto Setup Apply	0x00007940	0x00007950	Ways autoseup applied parameters area

Way Common settings

This area contains the way common settings.

BlockId	Start Address	End Address	Description
Is Autosetupable	0x00007000	0x00007010	This is the out autsetup capable of way - 1 if Autosetupable - else 0
Way gain	0x00007010	0x00007020	The way gain in linear
Way delay	0x00007020	0x00007030	The way delay in seconds
Way polarity	0x00007030	0x00007040	The way polarity
Way mute	0x00007040	0x00007050	The way mute
Way Name	0x00007050	0x00007090	Way Name
Way State	0x00007090	0x000070a0	Way State

Is Autosetupable

This is the out autsetup capable of way - 1 if Autosetupable - else 0

Offset	Name	Type	Dim	R \ W	Description
0x00007000 (28672)	Way 1 Is Autosetupable	uint32	4	RW	This is the out autsetup capable of way - 1 if Autosetupable - else 0
0x00007004 (28676)	Way 2 Is Autosetupable	uint32	4	RW	This is the out autsetup capable of way - 1 if Autosetupable - else 0
0x00007008 (28680)	Way 3 Is Autosetupable	uint32	4	RW	This is the out autsetup capable of way - 1 if Autosetupable - else 0
0x0000700c (28684)	Way 4 Is Autosetupable	uint32	4	RW	This is the out autsetup capable of way - 1 if Autosetupable - else 0

Way gain

The way gain in linear

Offset	Name	Type	Dim	R \ W	Description
0x00007010 (28688)	Way 1 gain	Float	4	RW	The way gain in linear
0x00007014 (28692)	Way 2 gain	Float	4	RW	The way gain in linear
0x00007018 (28696)	Way 3 gain	Float	4	RW	The way gain in linear
0x0000701c (28700)	Way 4 gain	Float	4	RW	The way gain in linear

Way delay

The way delay in seconds

Offset	Name	Type	Dim	R \ W	Description
0x00007020 (28704)	Way 1 delay	Float	4	R\W	The way delay in seconds
0x00007024 (28708)	Way 2 delay	Float	4	R\W	The way delay in seconds
0x00007028 (28712)	Way 3 delay	Float	4	R\W	The way delay in seconds
0x0000702c (28716)	Way 4 delay	Float	4	R\W	The way delay in seconds

Way polarity

The way polarity

Offset	Name	Type	Dim	R \ W	Description
0x00007030 (28720)	Way 1 polarity	uint32	4	R\W	The way polarity
0x00007034 (28724)	Way 2 polarity	uint32	4	R\W	The way polarity
0x00007038 (28728)	Way 3 polarity	uint32	4	R\W	The way polarity
0x0000703c (28732)	Way 4 polarity	uint32	4	R\W	The way polarity

Way mute

The way mute

Offset	Name	Type	Dim	R \ W	Description
0x00007040 (28736)	Way 1 mute	uint32	4	R\W	The way mute
0x00007044 (28740)	Way 2 mute	uint32	4	R\W	The way mute
0x00007048 (28744)	Way 3 mute	uint32	4	R\W	The way mute
0x0000704c (28748)	Way 4 mute	uint32	4	R\W	The way mute

Way Name

Way Name

Offset	Name	Type	Dim	R \ W	Description
0x00007050 (28752)	Way Name Way 1	char[16]	16	R\W	Way Name for way 1
0x00007060 (28768)	Way Name Way 2	char[16]	16	R\W	Way Name for way 2
0x00007070 (28784)	Way Name Way 3	char[16]	16	R\W	Way Name for way 3
0x00007080 (28800)	Way Name Way 4	char[16]	16	R\W	Way Name for way 4

Way State

Way State

Offset	Name	Type	Dim	R \ W	Description
0x00007090 (28816)	Way State Way 1	uint32	4	RW	Way State for way 1
0x00007094 (28820)	Way State Way 2	uint32	4	RW	Way State for way 2
0x00007098 (28824)	Way State Way 3	uint32	4	RW	Way State for way 3
0x0000709c (28828)	Way State Way 4	uint32	4	RW	Way State for way 4

Way Eq

This area contains the way equalizer.

BlockId	Start Address	End Address	Description
Way 1 Eq	0x00007200	0x00007298	This area contains the way 1 equalizer.
Way 2 Eq	0x00007298	0x00007330	This area contains the way 2 equalizer.
Way 3 Eq	0x00007330	0x000073c8	This area contains the way 3 equalizer.
Way 4 Eq	0x000073c8	0x00007460	This area contains the way 4 equalizer.

Way 1 Eq

This area contains the way 1 equalizer.

BlockId	Start Address	End Address	Description
Way 1 Eq XOver settings	0x00007200	0x00007220	This area contains the way equalizer cross over.
Way 1 Eq BiQuad 1 settings	0x00007220	0x00007238	This area contains the way equalizer biQuad settings.
Way 1 Eq BiQuad 2 settings	0x00007238	0x00007250	This area contains the way equalizer biQuad settings.
Way 1 Eq BiQuad 3 settings	0x00007250	0x00007268	This area contains the way equalizer biQuad settings.
Way 1 Eq BiQuad 4 settings	0x00007268	0x00007280	This area contains the way equalizer biQuad settings.
Way 1 Eq BiQuad 5 settings	0x00007280	0x00007298	This area contains the way equalizer biQuad settings.

Way 1 Eq XOver settings

This area contains the way equalizer cross over.

BlockId	Start Address	End Address	Description
Way 1 Hp filter	0x00007200	0x00007210	This area contains the way equalizer cross over.
Way 1 Lp filter	0x00007210	0x00007220	This area contains the way equalizer cross over.

Way 1 Hp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x00007200 (29184)	Enabled	uint32	4	R/W	The enable flag
0x00007204 (29188)	Type	uint32	4	R/W	The filter type
0x00007208 (29192)	Slope	uint32	4	R/W	The slope in db/oct
0x0000720c (29196)	Frequency	uint32	4	R/W	The filter frequency

Way 1 Lp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x00007210 (29200)	Enabled	uint32	4	R/W	The enable flag
0x00007214 (29204)	Type	uint32	4	R/W	The filter type
0x00007218 (29208)	Slope	uint32	4	R/W	The slope in db/oct
0x0000721c (29212)	Frequency	uint32	4	R/W	The filter frequency

Way 1 Eq BiQuad 1 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007220 (29216)	Enabled	uint32	4	R/W	The enable flag														
0x00007224 (29220)	Type	uint32	4	R/W	The filter type. Valid values are: <table border="1" data-bbox="935 1462 1214 1682"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
4	0x02																		
5	0x03																		
0x00007228 (29224)	Q	Float	4	R/W	The filter Q														
0x0000722c (29228)	Slope	Float	4	R/W	The filter Slope														
0x00007230 (29232)	Frequency	uint32	4	R/W	The filter frequency														
0x00007234 (29236)	Gain	Float	4	R/W	The linear gain														

Way 1 Eq BiQuad 2 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007238 (29240)	Enabled	uint32	4	RW	The enable flag														
0x0000723c (29244)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 477 1214 694"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007240 (29248)	Q	Float	4	RW	The filter Q														
0x00007244 (29252)	Slope	Float	4	RW	The filter Slope														
0x00007248 (29256)	Frequency	uint32	4	RW	The filter frequency														
0x0000724c (29260)	Gain	Float	4	RW	The linear gain														

Way 1 Eq BiQuad 3 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007250 (29264)	Enabled	uint32	4	RW	The enable flag														
0x00007254 (29268)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 1330 1214 1547"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007258 (29272)	Q	Float	4	RW	The filter Q														
0x0000725c (29276)	Slope	Float	4	RW	The filter Slope														
0x00007260 (29280)	Frequency	uint32	4	RW	The filter frequency														
0x00007264 (29284)	Gain	Float	4	RW	The linear gain														

Way 1 Eq BiQuad 4 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007268 (29288)	Enabled	uint32	4	RW	The enable flag														
0x0000726c (29292)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 477 1214 694"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007270 (29296)	Q	Float	4	RW	The filter Q														
0x00007274 (29300)	Slope	Float	4	RW	The filter Slope														
0x00007278 (29304)	Frequency	uint32	4	RW	The filter frequency														
0x0000727c (29308)	Gain	Float	4	RW	The linear gain														

Way 1 Eq BiQuad 5 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007280 (29312)	Enabled	uint32	4	RW	The enable flag														
0x00007284 (29316)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 1330 1214 1547"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
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5	0x03																		
0x00007288 (29320)	Q	Float	4	RW	The filter Q														
0x0000728c (29324)	Slope	Float	4	RW	The filter Slope														
0x00007290 (29328)	Frequency	uint32	4	RW	The filter frequency														
0x00007294 (29332)	Gain	Float	4	RW	The linear gain														

Way 2 Eq

This area contains the way 2 equalizer.

BlockId	Start Address	End Address	Description
Way 2 Eq XOver settings	0x00007298	0x000072b8	This area contains the way equalizer cross over.
Way 2 Eq BiQuad 1 settings	0x000072b8	0x000072d0	This area contains the way equalizer biQuad settings.
Way 2 Eq BiQuad 2 settings	0x000072d0	0x000072e8	This area contains the way equalizer biQuad settings.
Way 2 Eq BiQuad 3 settings	0x000072e8	0x00007300	This area contains the way equalizer biQuad settings.
Way 2 Eq BiQuad 4 settings	0x00007300	0x00007318	This area contains the way equalizer biQuad settings.
Way 2 Eq BiQuad 5 settings	0x00007318	0x00007330	This area contains the way equalizer biQuad settings.

Way 2 Eq XOver settings

This area contains the way equalizer cross over.

BlockId	Start Address	End Address	Description
Way 2 Hp filter	0x00007298	0x000072a8	This area contains the way equalizer cross over.
Way 2 Lp filter	0x000072a8	0x000072b8	This area contains the way equalizer cross over.

Way 2 Hp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x00007298 (29336)	Enabled	uint32	4	R\W	The enable flag
0x0000729c (29340)	Type	uint32	4	R\W	The filter type
0x000072a0 (29344)	Slope	uint32	4	R\W	The slope in db/oct
0x000072a4 (29348)	Frequency	uint32	4	R\W	The filter frequency

Way 2 Lp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x000072a8 (29352)	Enabled	uint32	4	R\W	The enable flag
0x000072ac (29356)	Type	uint32	4	R\W	The filter type
0x000072b0 (29360)	Slope	uint32	4	R\W	The slope in db/oct
0x000072b4 (29364)	Frequency	uint32	4	R\W	The filter frequency

Way 2 Eq BiQuad 1 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000072b8 (29368)	Enabled	uint32	4	RW	The enable flag														
0x000072bc (29372)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x000072c0 (29376)	Q	Float	4	RW	The filter Q														
0x000072c4 (29380)	Slope	Float	4	RW	The filter Slope														
0x000072c8 (29384)	Frequency	uint32	4	RW	The filter frequency														
0x000072cc (29388)	Gain	Float	4	RW	The linear gain														

Way 2 Eq BiQuad 2 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000072d0 (29392)	Enabled	uint32	4	RW	The enable flag														
0x000072d4 (29396)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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0x000072d8 (29400)	Q	Float	4	RW	The filter Q														
0x000072dc (29404)	Slope	Float	4	RW	The filter Slope														
0x000072e0 (29408)	Frequency	uint32	4	RW	The filter frequency														
0x000072e4 (29412)	Gain	Float	4	RW	The linear gain														

Way 2 Eq BiQuad 3 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000072e8 (29416)	Enabled	uint32	4	RW	The enable flag														
0x000072ec (29420)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 477 1214 694"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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0x000072f0 (29424)	Q	Float	4	RW	The filter Q														
0x000072f4 (29428)	Slope	Float	4	RW	The filter Slope														
0x000072f8 (29432)	Frequency	uint32	4	RW	The filter frequency														
0x000072fc (29436)	Gain	Float	4	RW	The linear gain														

Way 2 Eq BiQuad 4 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007300 (29440)	Enabled	uint32	4	RW	The enable flag														
0x00007304 (29444)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 1330 1214 1547"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
4	0x02																		
5	0x03																		
0x00007308 (29448)	Q	Float	4	RW	The filter Q														
0x0000730c (29452)	Slope	Float	4	RW	The filter Slope														
0x00007310 (29456)	Frequency	uint32	4	RW	The filter frequency														
0x00007314 (29460)	Gain	Float	4	RW	The linear gain														

Way 2 Eq BiQuad 5 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007318 (29464)	Enabled	uint32	4	R/W	The enable flag														
0x0000731c (29468)	Type	uint32	4	R/W	The filter type. Valid values are: <table border="1" data-bbox="938 477 1216 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
4	0x02																		
5	0x03																		
0x00007320 (29472)	Q	Float	4	R/W	The filter Q														
0x00007324 (29476)	Slope	Float	4	R/W	The filter Slope														
0x00007328 (29480)	Frequency	uint32	4	R/W	The filter frequency														
0x0000732c (29484)	Gain	Float	4	R/W	The linear gain														

Way 3 Eq

This area contains the way 3 equalizer.

BlockId	Start Address	End Address	Description
Way 3 Eq XOver settings	0x00007330	0x00007350	This area contains the way equalizer cross over.
Way 3 Eq BiQuad 1 settings	0x00007350	0x00007368	This area contains the way equalizer biQuad settings.
Way 3 Eq BiQuad 2 settings	0x00007368	0x00007380	This area contains the way equalizer biQuad settings.
Way 3 Eq BiQuad 3 settings	0x00007380	0x00007398	This area contains the way equalizer biQuad settings.
Way 3 Eq BiQuad 4 settings	0x00007398	0x000073b0	This area contains the way equalizer biQuad settings.
Way 3 Eq BiQuad 5 settings	0x000073b0	0x000073c8	This area contains the way equalizer biQuad settings.

Way 3 Eq XOver settings

This area contains the way equalizer cross over.

BlockId	Start Address	End Address	Description
Way 3 Hp filter	0x00007330	0x00007340	This area contains the way equalizer cross over.
Way 3 Lp filter	0x00007340	0x00007350	This area contains the way equalizer cross over.

Way 3 Hp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x00007330 (29488)	Enabled	uint32	4	R/W	The enable flag
0x00007334 (29492)	Type	uint32	4	R/W	The filter type
0x00007338 (29496)	Slope	uint32	4	R/W	The slope in db/oct
0x0000733c (29500)	Frequency	uint32	4	R/W	The filter frequency

Way 3 Lp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x00007340 (29504)	Enabled	uint32	4	R/W	The enable flag
0x00007344 (29508)	Type	uint32	4	R/W	The filter type
0x00007348 (29512)	Slope	uint32	4	R/W	The slope in db/oct
0x0000734c (29516)	Frequency	uint32	4	R/W	The filter frequency

Way 3 Eq BiQuad 1 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007350 (29520)	Enabled	uint32	4	R/W	The enable flag														
0x00007354 (29524)	Type	uint32	4	R/W	The filter type. Valid values are: <table border="1" data-bbox="935 1462 1214 1682"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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5	0x03																		
0x00007358 (29528)	Q	Float	4	R/W	The filter Q														
0x0000735c (29532)	Slope	Float	4	R/W	The filter Slope														
0x00007360 (29536)	Frequency	uint32	4	R/W	The filter frequency														
0x00007364 (29540)	Gain	Float	4	R/W	The linear gain														

Way 3 Eq BiQuad 2 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007368 (29544)	Enabled	uint32	4	R\W	The enable flag														
0x0000736c (29548)	Type	uint32	4	R\W	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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5	0x03																		
0x00007370 (29552)	Q	Float	4	R\W	The filter Q														
0x00007374 (29556)	Slope	Float	4	R\W	The filter Slope														
0x00007378 (29560)	Frequency	uint32	4	R\W	The filter frequency														
0x0000737c (29564)	Gain	Float	4	R\W	The linear gain														

Way 3 Eq BiQuad 3 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007380 (29568)	Enabled	uint32	4	R\W	The enable flag														
0x00007384 (29572)	Type	uint32	4	R\W	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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5	0x03																		
0x00007388 (29576)	Q	Float	4	R\W	The filter Q														
0x0000738c (29580)	Slope	Float	4	R\W	The filter Slope														
0x00007390 (29584)	Frequency	uint32	4	R\W	The filter frequency														
0x00007394 (29588)	Gain	Float	4	R\W	The linear gain														

Way 3 Eq BiQuad 4 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007398 (29592)	Enabled	uint32	4	RW	The enable flag														
0x0000739c (29596)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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3	0x01																		
4	0x02																		
5	0x03																		
0x000073a0 (29600)	Q	Float	4	RW	The filter Q														
0x000073a4 (29604)	Slope	Float	4	RW	The filter Slope														
0x000073a8 (29608)	Frequency	uint32	4	RW	The filter frequency														
0x000073ac (29612)	Gain	Float	4	RW	The linear gain														

Way 3 Eq BiQuad 5 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000073b0 (29616)	Enabled	uint32	4	RW	The enable flag														
0x000073b4 (29620)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
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5	0x03																		
0x000073b8 (29624)	Q	Float	4	RW	The filter Q														
0x000073bc (29628)	Slope	Float	4	RW	The filter Slope														
0x000073c0 (29632)	Frequency	uint32	4	RW	The filter frequency														
0x000073c4 (29636)	Gain	Float	4	RW	The linear gain														

Way 4 Eq

This area contains the way 4 equalizer.

BlockId	Start Address	End Address	Description
Way 4 Eq XOver settings	0x000073c8	0x000073e8	This area contains the way equalizer cross over.
Way 4 Eq BiQuad 1 settings	0x000073e8	0x00007400	This area contains the way equalizer biQuad settings.
Way 4 Eq BiQuad 2 settings	0x00007400	0x00007418	This area contains the way equalizer biQuad settings.
Way 4 Eq BiQuad 3 settings	0x00007418	0x00007430	This area contains the way equalizer biQuad settings.
Way 4 Eq BiQuad 4 settings	0x00007430	0x00007448	This area contains the way equalizer biQuad settings.
Way 4 Eq BiQuad 5 settings	0x00007448	0x00007460	This area contains the way equalizer biQuad settings.

Way 4 Eq XOver settings

This area contains the way equalizer cross over.

BlockId	Start Address	End Address	Description
Way 4 Hp filter	0x000073c8	0x000073d8	This area contains the way equalizer cross over.
Way 4 Lp filter	0x000073d8	0x000073e8	This area contains the way equalizer cross over.

Way 4 Hp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x000073c8 (29640)	Enabled	uint32	4	R\W	The enable flag
0x000073cc (29644)	Type	uint32	4	R\W	The filter type
0x000073d0 (29648)	Slope	uint32	4	R\W	The slope in db/oct
0x000073d4 (29652)	Frequency	uint32	4	R\W	The filter frequency

Way 4 Lp filter

This area contains the way equalizer cross over.

Offset	Name	Type	Dim	R \ W	Description
0x000073d8 (29656)	Enabled	uint32	4	R\W	The enable flag
0x000073dc (29660)	Type	uint32	4	R\W	The filter type
0x000073e0 (29664)	Slope	uint32	4	R\W	The slope in db/oct
0x000073e4 (29668)	Frequency	uint32	4	R\W	The filter frequency

Way 4 Eq BiQuad 1 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x000073e8 (29672)	Enabled	uint32	4	RW	The enable flag														
0x000073ec (29676)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 477 1214 696"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x000073f0 (29680)	Q	Float	4	RW	The filter Q														
0x000073f4 (29684)	Slope	Float	4	RW	The filter Slope														
0x000073f8 (29688)	Frequency	uint32	4	RW	The filter frequency														
0x000073fc (29692)	Gain	Float	4	RW	The linear gain														

Way 4 Eq BiQuad 2 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007400 (29696)	Enabled	uint32	4	RW	The enable flag														
0x00007404 (29700)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 1330 1214 1550"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007408 (29704)	Q	Float	4	RW	The filter Q														
0x0000740c (29708)	Slope	Float	4	RW	The filter Slope														
0x00007410 (29712)	Frequency	uint32	4	RW	The filter frequency														
0x00007414 (29716)	Gain	Float	4	RW	The linear gain														

Way 4 Eq BiQuad 3 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007418 (29720)	Enabled	uint32	4	RW	The enable flag														
0x0000741c (29724)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 477 1214 694"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007420 (29728)	Q	Float	4	RW	The filter Q														
0x00007424 (29732)	Slope	Float	4	RW	The filter Slope														
0x00007428 (29736)	Frequency	uint32	4	RW	The filter frequency														
0x0000742c (29740)	Gain	Float	4	RW	The linear gain														

Way 4 Eq BiQuad 4 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007430 (29744)	Enabled	uint32	4	RW	The enable flag														
0x00007434 (29748)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="935 1330 1214 1547"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007438 (29752)	Q	Float	4	RW	The filter Q														
0x0000743c (29756)	Slope	Float	4	RW	The filter Slope														
0x00007440 (29760)	Frequency	uint32	4	RW	The filter frequency														
0x00007444 (29764)	Gain	Float	4	RW	The linear gain														

Way 4 Eq BiQuad 5 settings

This area contains the way equalizer biQuad settings.

Offset	Name	Type	Dim	R \ W	Description														
0x00007448 (29768)	Enabled	uint32	4	RW	The enable flag														
0x0000744c (29772)	Type	uint32	4	RW	The filter type. Valid values are: <table border="1" data-bbox="938 474 1216 689"> <thead> <tr> <th>Values</th> <th>Type</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>Peaking</td> </tr> <tr> <td>1</td> <td>BandPass</td> </tr> <tr> <td>2</td> <td>0x84</td> </tr> <tr> <td>3</td> <td>0x01</td> </tr> <tr> <td>4</td> <td>0x02</td> </tr> <tr> <td>5</td> <td>0x03</td> </tr> </tbody> </table>	Values	Type	0	Peaking	1	BandPass	2	0x84	3	0x01	4	0x02	5	0x03
Values	Type																		
0	Peaking																		
1	BandPass																		
2	0x84																		
3	0x01																		
4	0x02																		
5	0x03																		
0x00007450 (29776)	Q	Float	4	RW	The filter Q														
0x00007454 (29780)	Slope	Float	4	RW	The filter Slope														
0x00007458 (29784)	Frequency	uint32	4	RW	The filter frequency														
0x0000745c (29788)	Gain	Float	4	RW	The linear gain														

Way Limiters

This area contains the way limiters.

BlockId	Start Address	End Address	Description
Way 1 Limiters	0x00007700	0x00007730	This area contains the way limiters.
Way 2 Limiters	0x00007730	0x00007760	This area contains the way limiters.
Way 3 Limiters	0x00007760	0x00007790	This area contains the way limiters.
Way 4 Limiters	0x00007790	0x000077c0	This area contains the way limiters.

Way 1 Limiters

This area contains the way limiters.

BlockId	Start Address	End Address	Description
Way 1 Voltage peak limiter	0x00007700	0x00007710	This area contains the way voltage peak limiter settings.
Way 1 Voltage RMS limiter	0x00007710	0x00007720	This area contains the way voltage rms limiter settings.
Way 1 Voltage Clip limiter	0x00007720	0x00007730	This area contains the way clip limiter settings.

Way 1 Voltage peak limiter

This area contains the way voltage peak limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007700 (30464)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007704 (30468)	Threshold	Float	4	R\W	
0x00007708 (30472)	Attack Time	Float	4	R\W	
0x0000770c (30476)	Release Time	Float	4	R\W	

Way 1 Voltage RMS limiter

This area contains the way voltage rms limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007710 (30480)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007714 (30484)	Threshold	Float	4	R\W	
0x00007718 (30488)	Attack Time	Float	4	R\W	
0x0000771c (30492)	Release Time	Float	4	R\W	

Way 1 Voltage Clip limiter

This area contains the way clip limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007720 (30496)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007724 (30500)	Threshold	Float	4	R\W	
0x00007728 (30504)	Attack Time	Float	4	R\W	
0x0000772c (30508)	Release Time	Float	4	R\W	

Way 2 Limiters

This area contains the way limiters.

BlockId	Start Address	End Address	Description
Way 2 Voltage peak limiter	0x00007730	0x00007740	This area contains the way voltage peak limiter settings.
Way 2 Voltage RMS limiter	0x00007740	0x00007750	This area contains the way voltage rms limiter settings.
Way 2 Voltage Clip limiter	0x00007750	0x00007760	This area contains the way clip limiter settings.

Way 2 Voltage peak limiter

This area contains the way voltage peak limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007730 (30512)	Enable	uint32	4	R/W	If enabled is set to 1 - else 0
0x00007734 (30516)	Threshold	Float	4	R/W	
0x00007738 (30520)	Attack Time	Float	4	R/W	
0x0000773c (30524)	Release Time	Float	4	R/W	

Way 2 Voltage RMS limiter

This area contains the way voltage rms limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007740 (30528)	Enable	uint32	4	R/W	If enabled is set to 1 - else 0
0x00007744 (30532)	Threshold	Float	4	R/W	
0x00007748 (30536)	Attack Time	Float	4	R/W	
0x0000774c (30540)	Release Time	Float	4	R/W	

Way 2 Voltage Clip limiter

This area contains the way clip limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007750 (30544)	Enable	uint32	4	R/W	If enabled is set to 1 - else 0
0x00007754 (30548)	Threshold	Float	4	R/W	
0x00007758 (30552)	Attack Time	Float	4	R/W	
0x0000775c (30556)	Release Time	Float	4	R/W	

Way 3 Limiters

This area contains the way limiters.

BlockId	Start Address	End Address	Description
Way 3 Voltage peak limiter	0x00007760	0x00007770	This area contains the way voltage peak limiter settings.
Way 3 Voltage RMS limiter	0x00007770	0x00007780	This area contains the way voltage rms limiter settings.
Way 3 Voltage Clip limiter	0x00007780	0x00007790	This area contains the way clip limiter settings.

Way 3 Voltage peak limiter

This area contains the way voltage peak limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007760 (30560)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007764 (30564)	Threshold	Float	4	R\W	
0x00007768 (30568)	Attack Time	Float	4	R\W	
0x0000776c (30572)	Release Time	Float	4	R\W	

Way 3 Voltage RMS limiter

This area contains the way voltage rms limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007770 (30576)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007774 (30580)	Threshold	Float	4	R\W	
0x00007778 (30584)	Attack Time	Float	4	R\W	
0x0000777c (30588)	Release Time	Float	4	R\W	

Way 3 Voltage Clip limiter

This area contains the way clip limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007780 (30592)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007784 (30596)	Threshold	Float	4	R\W	
0x00007788 (30600)	Attack Time	Float	4	R\W	
0x0000778c (30604)	Release Time	Float	4	R\W	

Way 4 Limiters

This area contains the way limiters.

BlockId	Start Address	End Address	Description
Way 4 Voltage peak limiter	0x00007790	0x000077a0	This area contains the way voltage peak limiter settings.
Way 4 Voltage RMS limiter	0x000077a0	0x000077b0	This area contains the way voltage rms limiter settings.
Way 4 Voltage Clip limiter	0x000077b0	0x000077c0	This area contains the way clip limiter settings.

Way 4 Voltage peak limiter

This area contains the way voltage peak limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007790 (30608)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x00007794 (30612)	Threshold	Float	4	R\W	
0x00007798 (30616)	Attack Time	Float	4	R\W	
0x0000779c (30620)	Release Time	Float	4	R\W	

Way 4 Voltage RMS limiter

This area contains the way voltage rms limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x000077a0 (30624)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x000077a4 (30628)	Threshold	Float	4	R\W	
0x000077a8 (30632)	Attack Time	Float	4	R\W	
0x000077ac (30636)	Release Time	Float	4	R\W	

Way 4 Voltage Clip limiter

This area contains the way clip limiter settings.

Offset	Name	Type	Dim	R \ W	Description
0x000077b0 (30640)	Enable	uint32	4	R\W	If enabled is set to 1 - else 0
0x000077b4 (30644)	Threshold	Float	4	R\W	
0x000077b8 (30648)	Attack Time	Float	4	R\W	
0x000077bc (30652)	Release Time	Float	4	R\W	

Way Damping Control

This area contains the way damping control settings.

BlockId	Start Address	End Address	Description
Way 1 Damping Control	0x00007850	0x00007860	This area contains the way damping control settings.
Way 2 Damping Control	0x00007860	0x00007870	This area contains the way damping control settings.
Way 3 Damping Control	0x00007870	0x00007880	This area contains the way damping control settings.
Way 4 Damping Control	0x00007880	0x00007890	This area contains the way damping control settings.

Way 1 Damping Control

This area contains the way damping control settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007850 (30800)	Way 1 Damping Control Enable	uint32	4	R\W	This is the out damping control enable of way 1 - 1 if bridged - else 0
0x00007854 (30804)	Way 1 Damping Control Value	Float	4	R\W	This is the out damping control damping factor of way 1
0x00007858 (30808)	Way 1 Damping Control Spare	uint8[8]	8	R\W	This is the out damping control spare of way 1

Way 2 Damping Control

This area contains the way damping control settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007860 (30816)	Way 2 Damping Control Enable	uint32	4	R\W	This is the out damping control enable of way 2 - 1 if bridged - else 0
0x00007864 (30820)	Way 2 Damping Control Value	Float	4	R\W	This is the out damping control damping factor of way 2
0x00007868 (30824)	Way 2 Damping Control Spare	uint8[8]	8	R\W	This is the out damping control spare of way 2

Way 3 Damping Control

This area contains the way damping control settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007870 (30832)	Way 3 Damping Control Enable	uint32	4	R\W	This is the out damping control enable of way 3 - 1 if bridged - else 0
0x00007874 (30836)	Way 3 Damping Control Value	Float	4	R\W	This is the out damping control damping factor of way 3
0x00007878 (30840)	Way 3 Damping Control Spare	uint8[8]	8	R\W	This is the out damping control spare of way 3

Way 4 Damping Control

This area contains the way damping control settings.

Offset	Name	Type	Dim	R \ W	Description
0x00007880 (30848)	Way 4 Damping Control Enable	uint32	4	R\W	This is the out damping control enable of way 4 - 1 if bridged - else 0
0x00007884 (30852)	Way 4 Damping Control Value	Float	4	R\W	This is the out damping control damping factor of way 4
0x00007888 (30856)	Way 4 Damping Control Spare	uint8[8]	8	R\W	This is the out damping control spare of way 4

Diagnostic

Ways Diagnostic area

BlockId	Start Address	End Address	Description
Diagnostic Pilot Tone Generator	0x00007890	0x000078b4	Pilot Tone Generator area
Diagnostic Pilot Tone	0x000078b4	0x000078e8	Pilot Tone area
Diagnostic Load Monitor	0x000078e8	0x0000791c	Load Monitor area
Diagnostic Load Detect	0x0000791c	0x00007940	Load Detect area

Diagnostic Pilot Tone Generator

Pilot Tone Generator area

BlockId	Start Address	End Address	Description
Way Pilot Tone Generator Enable	0x00007890	0x00007894	Pilot Tone Generator Enable for way
Way Pilot Tone Generator Frequency	0x00007894	0x000078a4	Pilot Tone Generator Frequency for way
Way Pilot Tone Generator Amplitude	0x000078a4	0x000078b4	Pilot Tone Generator Amplitude for way

Way Pilot Tone Generator Enable

Pilot Tone Generator Enable for way

Offset	Name	Type	Dim	R \ W	Description
0x00007890 (30864)	Way 1 Pilot Tone Generator Enable	uint8	1	RW	Pilot Tone Generator Enable for way 1
0x00007891 (30865)	Way 2 Pilot Tone Generator Enable	uint8	1	RW	Pilot Tone Generator Enable for way 2
0x00007892 (30866)	Way 3 Pilot Tone Generator Enable	uint8	1	RW	Pilot Tone Generator Enable for way 3
0x00007893 (30867)	Way 4 Pilot Tone Generator Enable	uint8	1	RW	Pilot Tone Generator Enable for way 4

Way Pilot Tone Generator Frequency

Pilot Tone Generator Frequency for way

Offset	Name	Type	Dim	R \ W	Description
0x00007894 (30868)	Way 1 Pilot Tone Generator Frequency	uint32	4	RW	Pilot Tone Generator Frequency for way 1
0x00007898 (30872)	Way 2 Pilot Tone Generator Frequency	uint32	4	RW	Pilot Tone Generator Frequency for way 2
0x0000789c (30876)	Way 3 Pilot Tone Generator Frequency	uint32	4	RW	Pilot Tone Generator Frequency for way 3
0x000078a0 (30880)	Way 4 Pilot Tone Generator Frequency	uint32	4	RW	Pilot Tone Generator Frequency for way 4

Way Pilot Tone Generator Amplitude

Pilot Tone Generator Amplitude for way

Offset	Name	Type	Dim	R \ W	Description
0x000078a4 (30884)	Way 1 Pilot Tone Generator Amplitude	Float	4	RW	Pilot Tone Generator Amplitude for way 1
0x000078a8 (30888)	Way 2 Pilot Tone Generator Amplitude	Float	4	RW	Pilot Tone Generator Amplitude for way 2
0x000078ac (30892)	Way 3 Pilot Tone Generator Amplitude	Float	4	RW	Pilot Tone Generator Amplitude for way 3
0x000078b0 (30896)	Way 4 Pilot Tone Generator Amplitude	Float	4	RW	Pilot Tone Generator Amplitude for way 4

Diagnostic Pilot Tone

Pilot Tone area

BlockId	Start Address	End Address	Description
Way Pilot Tone Enable	0x000078b4	0x000078b8	Pilot Tone area
Way Pilot Tone Frequency	0x000078b8	0x000078c8	Pilot Tone Frequency for way
Way Pilot Tone Lowth	0x000078c8	0x000078d8	Pilot Tone Lowth for way
Way Pilot Tone Highth	0x000078d8	0x000078e8	Pilot Tone Highth for way

Way Pilot Tone Enable

Pilot Tone area

Offset	Name	Type	Dim	R \ W	Description
0x000078b4 (30900)	Way 1 Pilot Tone Enable	uint8	1	RW	Pilot Tone Enable for way 1
0x000078b5 (30901)	Way 2 Pilot Tone Enable	uint8	1	RW	Pilot Tone Enable for way 2
0x000078b6 (30902)	Way 3 Pilot Tone Enable	uint8	1	RW	Pilot Tone Enable for way 3
0x000078b7 (30903)	Way 4 Pilot Tone Enable	uint8	1	RW	Pilot Tone Enable for way 4

Way Pilot Tone Frequency

Pilot Tone Frequency for way

Offset	Name	Type	Dim	R \ W	Description
0x000078b8 (30904)	Way 1 Pilot Tone Frequency	uint32	4	RW	Pilot Tone Frequency for way 1
0x000078bc (30908)	Way 2 Pilot Tone Frequency	uint32	4	RW	Pilot Tone Frequency for way 2
0x000078c0 (30912)	Way 3 Pilot Tone Frequency	uint32	4	RW	Pilot Tone Frequency for way 3
0x000078c4 (30916)	Way 4 Pilot Tone Frequency	uint32	4	RW	Pilot Tone Frequency for way 4

Way Pilot Tone Lowth

Pilot Tone Lowth for way

Offset	Name	Type	Dim	R \ W	Description
0x000078c8 (30920)	Way 1 Pilot Tone Lowth	Float	4	RW	Pilot Tone Lowth for way 1
0x000078cc (30924)	Way 2 Pilot Tone Lowth	Float	4	RW	Pilot Tone Lowth for way 2
0x000078d0 (30928)	Way 3 Pilot Tone Lowth	Float	4	RW	Pilot Tone Lowth for way 3
0x000078d4 (30932)	Way 4 Pilot Tone Lowth	Float	4	RW	Pilot Tone Lowth for way 4

Way Pilot Tone Highth

Pilot Tone Highth for way

Offset	Name	Type	Dim	R \ W	Description
0x000078d8 (30936)	Way 1 Pilot Tone Highth	Float	4	RW	Pilot Tone Highth for way 1
0x000078dc (30940)	Way 2 Pilot Tone Highth	Float	4	RW	Pilot Tone Highth for way 2
0x000078e0 (30944)	Way 3 Pilot Tone Highth	Float	4	RW	Pilot Tone Highth for way 3
0x000078e4 (30948)	Way 4 Pilot Tone Highth	Float	4	RW	Pilot Tone Highth for way 4

Diagnostic Load Monitor

Load Monitor area

BlockId	Start Address	End Address	Description
Way Load Monitor Enable	0x000078e8	0x000078ec	Load Monitor Enable for way
Way Load Monitor Frequency	0x000078ec	0x000078fc	Load Monitor Frequency for way
Way Load Monitor Lowth	0x000078fc	0x0000790c	Load Monitor Lowth for way
Way Load Monitor Highth	0x0000790c	0x0000791c	Load Monitor Highth for way

Way Load Monitor Enable

Load Monitor Enable for way

Offset	Name	Type	Dim	R \ W	Description
0x000078e8 (30952)	Way Load Monitor Enable Way 1	uint8	1	RW	Load Monitor Enable for way 1
0x000078e9 (30953)	Way Load Monitor Enable Way 2	uint8	1	RW	Load Monitor Enable for way 2
0x000078ea (30954)	Way Load Monitor Enable Way 3	uint8	1	RW	Load Monitor Enable for way 3
0x000078eb (30955)	Way Load Monitor Enable Way 4	uint8	1	RW	Load Monitor Enable for way 4

Way Load Monitor Frequency

Load Monitor Frequency for way

Offset	Name	Type	Dim	R \ W	Description
0x000078ec (30956)	Way Load Monitor Frequency Way 1	uint32	4	R\W	Load Monitor Frequency for way 1
0x000078f0 (30960)	Way Load Monitor Frequency Way 2	uint32	4	R\W	Load Monitor Frequency for way 2
0x000078f4 (30964)	Way Load Monitor Frequency Way 3	uint32	4	R\W	Load Monitor Frequency for way 3
0x000078f8 (30968)	Way Load Monitor Frequency Way 4	uint32	4	R\W	Load Monitor Frequency for way 4

Way Load Monitor Lowth

Load Monitor Lowth for way

Offset	Name	Type	Dim	R \ W	Description
0x000078fc (30972)	Way Load Monitor Lowth Way 1	Float	4	R\W	Load Monitor Lowth for way 1
0x00007900 (30976)	Way Load Monitor Lowth Way 2	Float	4	R\W	Load Monitor Lowth for way 2
0x00007904 (30980)	Way Load Monitor Lowth Way 3	Float	4	R\W	Load Monitor Lowth for way 3
0x00007908 (30984)	Way Load Monitor Lowth Way 4	Float	4	R\W	Load Monitor Lowth for way 4

Way Load Monitor Highth

Load Monitor Highth for way

Offset	Name	Type	Dim	R \ W	Description
0x0000790c (30988)	Way Load Monitor Highth Way 1	Float	4	R\W	Load Monitor Highth for way 1
0x00007910 (30992)	Way Load Monitor Highth Way 2	Float	4	R\W	Load Monitor Highth for way 2
0x00007914 (30996)	Way Load Monitor Highth Way 3	Float	4	R\W	Load Monitor Highth for way 3
0x00007918 (31000)	Way Load Monitor Highth Way 4	Float	4	R\W	Load Monitor Highth for way 4

Diagnostic Load Detect

Load Detect area

BlockId	Start Address	End Address	Description
Way Load Detect Enable	0x0000791c	0x00007920	Load Detect Enable
Way Load Detect Lowth	0x00007920	0x00007930	Load Detect Lowth
Way Load Detect Highth	0x00007930	0x00007940	Load Detect Highth

Way Load Detect Enable

Load Detect Enable

Offset	Name	Type	Dim	R \ W	Description
0x0000791c (31004)	Way Load Detect Enable Way 1	uint8	1	R\W	Load Detect Enable for way 1
0x0000791d (31005)	Way Load Detect Enable Way 2	uint8	1	R\W	Load Detect Enable for way 2
0x0000791e (31006)	Way Load Detect Enable Way 3	uint8	1	R\W	Load Detect Enable for way 3
0x0000791f (31007)	Way Load Detect Enable Way 4	uint8	1	R\W	Load Detect Enable for way 4

Way Load Detect Lowth

Load Detect Lowth

Offset	Name	Type	Dim	R \ W	Description
0x00007920 (31008)	Way Load Detect Lowth Way 1	Float	4	R\W	Load Detect Lowth for way 1
0x00007924 (31012)	Way Load Detect Lowth Way 2	Float	4	R\W	Load Detect Lowth for way 2
0x00007928 (31016)	Way Load Detect Lowth Way 3	Float	4	R\W	Load Detect Lowth for way 3
0x0000792c (31020)	Way Load Detect Lowth Way 4	Float	4	R\W	Load Detect Lowth for way 4

Way Load Detect Highth

Load Detect Highth

Offset	Name	Type	Dim	R \ W	Description
0x00007930 (31024)	Way Load Detect Highth Way 1	Float	4	R\W	Load Detect Highth for way 1
0x00007934 (31028)	Way Load Detect Highth Way 2	Float	4	R\W	Load Detect Highth for way 2
0x00007938 (31032)	Way Load Detect Highth Way 3	Float	4	R\W	Load Detect Highth for way 3
0x0000793c (31036)	Way Load Detect Highth Way 4	Float	4	R\W	Load Detect Highth for way 4

Auto Setup Apply

Ways autoseup applied parameters area

BlockId	Start Address	End Address	Description
Gain	0x00007940	0x00007950	The gain

Gain

The gain

Offset	Name	Type	Dim	R \ W	Description
0x00007940 (31040)	AutoSetup Gain Way 1	float	4	RW	AutoSetup Gain for way 1
0x00007944 (31044)	AutoSetup Gain Way 2	float	4	RW	AutoSetup Gain for way 2
0x00007948 (31048)	AutoSetup Gain Way 3	float	4	RW	AutoSetup Gain for way 3
0x0000794c (31052)	AutoSetup Gain Way 4	float	4	RW	AutoSetup Gain for way 4

Dante routing

This area contains informations related to the amplifiers sources.

BlockId	Start Address	End Address	Description
Gain	0x00008500	0x00008510	Dante route gain for output
DspTapType	0x00008510	0x00008514	Dante route dsp tap type for output
Channel	0x00008514	0x00008518	Dante route channel for output

Gain

Dante route gain for output

Offset	Name	Type	Dim	R \ W	Description
0x00008500 (34048)	Out 1 Dante Route Gain	float	4	RW	Dante route gain for output 1
0x00008504 (34052)	Out 2 Dante Route Gain	float	4	RW	Dante route gain for output 2
0x00008508 (34056)	Out 3 Dante Route Gain	float	4	RW	Dante route gain for output 3
0x0000850c (34060)	Out 4 Dante Route Gain	float	4	RW	Dante route gain for output 4

DspTapType

Dante route dsp tap type for output

Offset	Name	Type	Dim	R \ W	Description
0x00008510 (34064)	Out 1 Dante Route DspTapType	uint8	1	RW	Dante route dsp tap type for output 1
0x00008511 (34065)	Out 2 Dante Route DspTapType	uint8	1	RW	Dante route dsp tap type for output 2
0x00008512 (34066)	Out 3 Dante Route DspTapType	uint8	1	RW	Dante route dsp tap type for output 3
0x00008513 (34067)	Out 4 Dante Route DspTapType	uint8	1	RW	Dante route dsp tap type for output 4

Channel

Dante route channel for output

Offset	Name	Type	Dim	R \ W	Description
0x00008514 (34068)	Out 1 Dante Route Channel	uint8	1	RW	Dante route channel for output 1
0x00008515 (34069)	Out 2 Dante Route Channel	uint8	1	RW	Dante route channel for output 2
0x00008516 (34070)	Out 3 Dante Route Channel	uint8	1	RW	Dante route channel for output 3
0x00008517 (34071)	Out 4 Dante Route Channel	uint8	1	RW	Dante route channel for output 4

GPI configuration????

This area contains informations related to the amplifiers sources.

Offset	Name	Type	Dim	R \ W	Description
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Power config

This area contains informations related to the amplifiers sources.

BlockId	Start Address	End Address	Description
Standby	0x0000a000	0x0000a008	Amplifier stanby commands

Standby

Amplifier stanby commands

Offset	Name	Type	Dim	R \ W	Description
0x0000a000 (40960)	Standby trigger	uint32	4	W	If set to '1' activates the standby if set to '0' deactivates it
0x0000a004 (40964)	Auto turn on enable	uint32	4	RW	If set to '1' activates the auto turn on if set to '0' deactivates it

Readings

This area contains informations related to the amplifiers sources.

BlockId	Start Address	End Address	Description
Slow Meters	0x0000b000	0x0000b658	This area contains the slow meters.
Fast Meters	0x0000b800	0x0000bbd0	This area contains the fast meters.

Slow Meters

This area contains the slow meters.

BlockId	Start Address	End Address	Description
SlowMeter Ali	0x0000b000	0x0000b020	This area contains the Ali slow meters.
SlowMeter Ampli Channels	0x0000b0f4	0x0000b104	This area contains the Ampli slow meters.
SlowMeter External	0x0000b220	0x0000b224	This area contains the External slow meters.
SlowMeter SourceSelection	0x0000b300	0x0000b338	This area contains the SourceSelection slow meters.
SlowMeter Ways	0x0000b478	0x0000b4d8	This area contains the Ways slow meters.
SlowMeter PreWays	0x0000b5e8	0x0000b5f8	This area contains the PreWays slow meters.
SlowMeter Auto Standby	0x0000b638	0x0000b63c	This area contains the Auto Standby slow meters.
SlowMeter ChannelAlarmStatuses	0x0000b63c	0x0000b650	This area contains the ChannelAlarmStatuses slow meters.
SlowMeter AlarmStatus	0x0000b650	0x0000b658	This area contains the AlarmStatus slow meters.

SlowMeter Ali

This area contains the Ali slow meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000b000 (45056)	TempTrasf	Float	4	R	Reads the TempTrasf
0x0000b004 (45060)	TempHeatSink	Float	4	R	Reads the TempHeatSink
0x0000b008 (45064)	VMainsRms	Float	4	R	Reads the VMainsRms
0x0000b00c (45068)	VccP	Float	4	R	Reads the VccP
0x0000b010 (45072)	VccN	Float	4	R	Reads the VccN
0x0000b014 (45076)	FanCurrent	Float	4	R	Reads the FanCurrent
0x0000b018 (45080)	VAuxP	Float	4	R	Reads the VAuxP
0x0000b01c (45084)	VAuxN	Float	4	R	Reads the VAuxN

SlowMeter Ampli Channels

This area contains the Ampli slow meters.

BlockId	Start Address	End Address	Description
SlowMeter Ampli	0x0000b0f4	0x0000b104	This area contains the Ampli slow meters.

SlowMeter Ampli

This area contains the Ampli slow meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000b0f4 (45300)	TempCh 1	Float	4	R	Reads the TempCh
0x0000b0f8 (45304)	TempCh 2	Float	4	R	Reads the TempCh
0x0000b0fc (45308)	TempCh 3	Float	4	R	Reads the TempCh
0x0000b100 (45312)	TempCh 4	Float	4	R	Reads the TempCh

SlowMeter External

This area contains the External slow meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000b220 (45600)	TempAmbPeak	Float	4	R	Reads the TempAmbPeak

SlowMeter SourceSelection

This area contains the SourceSelection slow meters.

BlockId	Start Address	End Address	Description
SelectedBackup	0x0000b300	0x0000b310	Reads SelectedBackup TempUpPeak
PilotToneDetectActual0	0x0000b310	0x0000b320	Reads the PilotToneDetectActual0
PilotToneDetectActual1	0x0000b320	0x0000b330	Reads the PilotToneDetectActual1
PilotToneDetectedActual0	0x0000b330	0x0000b334	Reads the PilotToneDetectedActual0
PilotToneDetectedActual1	0x0000b334	0x0000b338	Reads the PilotToneDetectedActual1

SelectedBackup

Reads SelectedBackup TempUpPeak

Offset	Name	Type	Dim	R \ W	Description
0x0000b300 (45824)	SelectedBackup Source Selection CH 1	uint32	4	R	Reads SelectedBackup TempUpPeak
0x0000b304 (45828)	SelectedBackup Source Selection CH 2	uint32	4	R	Reads SelectedBackup TempUpPeak
0x0000b308 (45832)	SelectedBackup Source Selection CH 3	uint32	4	R	Reads SelectedBackup TempUpPeak
0x0000b30c (45836)	SelectedBackup Source Selection CH 4	uint32	4	R	Reads SelectedBackup TempUpPeak

PilotToneDetectActual0

Reads the PilotToneDetectActual0

Offset	Name	Type	Dim	R \ W	Description
0x0000b310 (45840)	PilotToneDetectActual0 Source Selection CH 1	Float	4	R	Reads the PilotToneDetectActual0
0x0000b314 (45844)	PilotToneDetectActual0 Source Selection CH 2	Float	4	R	Reads the PilotToneDetectActual0
0x0000b318 (45848)	PilotToneDetectActual0 Source Selection CH 3	Float	4	R	Reads the PilotToneDetectActual0
0x0000b31c (45852)	PilotToneDetectActual0 Source Selection CH 4	Float	4	R	Reads the PilotToneDetectActual0

PilotToneDetectActual1

Reads the PilotToneDetectActual1

Offset	Name	Type	Dim	R \ W	Description
0x0000b320 (45856)	PilotToneDetectActual1 Source Selection CH 1	Float	4	R	Reads the PilotToneDetectActual1
0x0000b324 (45860)	PilotToneDetectActual1 Source Selection CH 2	Float	4	R	Reads the PilotToneDetectActual1
0x0000b328 (45864)	PilotToneDetectActual1 Source Selection CH 3	Float	4	R	Reads the PilotToneDetectActual1
0x0000b32c (45868)	PilotToneDetectActual1 Source Selection CH 4	Float	4	R	Reads the PilotToneDetectActual1

PilotToneDetectedActual0

Reads the PilotToneDetectedActual0

Offset	Name	Type	Dim	R \ W	Description
0x0000b330 (45872)	PilotToneDetectedActual0 Source Selection CH 1	uint8	1	R	Reads the PilotToneDetectedActual0
0x0000b331 (45873)	PilotToneDetectedActual0 Source Selection CH 2	uint8	1	R	Reads the PilotToneDetectedActual0
0x0000b332 (45874)	PilotToneDetectedActual0 Source Selection CH 3	uint8	1	R	Reads the PilotToneDetectedActual0
0x0000b333 (45875)	PilotToneDetectedActual0 Source Selection CH 4	uint8	1	R	Reads the PilotToneDetectedActual0

PilotToneDetectedActual1

Reads the PilotToneDetectedActual1

Offset	Name	Type	Dim	R \ W	Description
0x0000b334 (45876)	PilotToneDetectedActual1 Source Selection CH 1	uint8	1	R	Reads the PilotToneDetectedActual1
0x0000b335 (45877)	PilotToneDetectedActual1 Source Selection CH 2	uint8	1	R	Reads the PilotToneDetectedActual1
0x0000b336 (45878)	PilotToneDetectedActual1 Source Selection CH 3	uint8	1	R	Reads the PilotToneDetectedActual1
0x0000b337 (45879)	PilotToneDetectedActual1 Source Selection CH 4	uint8	1	R	Reads the PilotToneDetectedActual1

SlowMeter Ways

This area contains the Ways slow meters.

BlockId	Start Address	End Address	Description
PilotToneDetection Ways	0x0000b478	0x0000b488	Reads the PilotToneDetection
PilotToneDetectionImpedance Ways	0x0000b488	0x0000b498	Reads the PilotToneDetectionImpedance
NominalImpedanceDetection Ways	0x0000b498	0x0000b4a8	Reads the NominalImpedanceDetection
PilotToneDetectedRms Ways	0x0000b4a8	0x0000b4ac	Reads the PilotToneDetectedRms
PilotToneDetectedImpedance Ways	0x0000b4ac	0x0000b4b0	Reads the PilotToneDetectedImpedance
NominalImpedanceDetected Ways	0x0000b4b0	0x0000b4b4	Reads the NominalImpedanceDetected
PilotToneDetectionRmsIsValid Ways	0x0000b4b4	0x0000b4b8	Reads the PilotToneDetectionRmsIsValid
PilotToneDetectionImpedanceRmsIsValid Ways	0x0000b4b8	0x0000b4c8	Reads the PilotToneDetectionImpedanceRmsIsValid
DetectionNominalImpedanceRmsIsValid Ways	0x0000b4c8	0x0000b4d8	Reads the DetectionNominalImpedanceRmsIsValid

PilotToneDetection Ways

Reads the PilotToneDetection

Offset	Name	Type	Dim	R \ W	Description
0x0000b478 (46200)	PilotToneDetection Ways CH 1	Float	4	R	Reads the PilotToneDetection
0x0000b47c (46204)	PilotToneDetection Ways CH 2	Float	4	R	Reads the PilotToneDetection
0x0000b480 (46208)	PilotToneDetection Ways CH 3	Float	4	R	Reads the PilotToneDetection
0x0000b484 (46212)	PilotToneDetection Ways CH 4	Float	4	R	Reads the PilotToneDetection

PilotToneDetectionImpedance Ways

Reads the PilotToneDetectionImpedance

Offset	Name	Type	Dim	R \ W	Description
0x0000b488 (46216)	PilotToneDetectionImpedance Ways CH 1	Float	4	R	Reads the PilotToneDetectionImpedance
0x0000b48c (46220)	PilotToneDetectionImpedance Ways CH 2	Float	4	R	Reads the PilotToneDetectionImpedance
0x0000b490 (46224)	PilotToneDetectionImpedance Ways CH 3	Float	4	R	Reads the PilotToneDetectionImpedance
0x0000b494 (46228)	PilotToneDetectionImpedance Ways CH 4	Float	4	R	Reads the PilotToneDetectionImpedance

NominalImpedanceDetection Ways

Reads the NominalImpedanceDetection

Offset	Name	Type	Dim	R \ W	Description
0x0000b498 (46232)	NominalImpedanceDetection Ways CH 1	Float	4	R	Reads the NominalImpedanceDetection
0x0000b49c (46236)	NominalImpedanceDetection Ways CH 2	Float	4	R	Reads the NominalImpedanceDetection
0x0000b4a0 (46240)	NominalImpedanceDetection Ways CH 3	Float	4	R	Reads the NominalImpedanceDetection
0x0000b4a4 (46244)	NominalImpedanceDetection Ways CH 4	Float	4	R	Reads the NominalImpedanceDetection

PilotToneDetectedRms Ways

Reads the PilotToneDetectedRms

Offset	Name	Type	Dim	R \ W	Description
0x0000b4a8 (46248)	PilotToneDetectedRms Ways CH 1	uint8	1	R	Reads the PilotToneDetectedRms
0x0000b4a9 (46249)	PilotToneDetectedRms Ways CH 2	uint8	1	R	Reads the PilotToneDetectedRms
0x0000b4aa (46250)	PilotToneDetectedRms Ways CH 3	uint8	1	R	Reads the PilotToneDetectedRms
0x0000b4ab (46251)	PilotToneDetectedRms Ways CH 4	uint8	1	R	Reads the PilotToneDetectedRms

PilotToneDetectedImpedance Ways

Reads the PilotToneDetectedImpedance

Offset	Name	Type	Dim	R \ W	Description
0x0000b4ac (46252)	PilotToneDetectedImpedance Ways CH 1	uint8	1	R	Reads the PilotToneDetectedImpedance
0x0000b4ad (46253)	PilotToneDetectedImpedance Ways CH 2	uint8	1	R	Reads the PilotToneDetectedImpedance
0x0000b4ae (46254)	PilotToneDetectedImpedance Ways CH 3	uint8	1	R	Reads the PilotToneDetectedImpedance
0x0000b4af (46255)	PilotToneDetectedImpedance Ways CH 4	uint8	1	R	Reads the PilotToneDetectedImpedance

NominallImpedanceDetected Ways

Reads the NominallImpedanceDetected

Offset	Name	Type	Dim	R \ W	Description
0x0000b4b0 (46256)	NominallImpedanceDetected Ways CH 1	uint8	1	R	Reads the NominallImpedanceDetected
0x0000b4b1 (46257)	NominallImpedanceDetected Ways CH 2	uint8	1	R	Reads the NominallImpedanceDetected
0x0000b4b2 (46258)	NominallImpedanceDetected Ways CH 3	uint8	1	R	Reads the NominallImpedanceDetected
0x0000b4b3 (46259)	NominallImpedanceDetected Ways CH 4	uint8	1	R	Reads the NominallImpedanceDetected

PilotToneDetectionRmslsValid Ways

Reads the PilotToneDetectionRmslsValid

Offset	Name	Type	Dim	R \ W	Description
0x0000b4b4 (46260)	PilotToneDetectionRmslsValid Ways CH 1	uint8	1	R	Reads the PilotToneDetectionRmslsValid
0x0000b4b5 (46261)	PilotToneDetectionRmslsValid Ways CH 2	uint8	1	R	Reads the PilotToneDetectionRmslsValid
0x0000b4b6 (46262)	PilotToneDetectionRmslsValid Ways CH 3	uint8	1	R	Reads the PilotToneDetectionRmslsValid
0x0000b4b7 (46263)	PilotToneDetectionRmslsValid Ways CH 4	uint8	1	R	Reads the PilotToneDetectionRmslsValid

PilotToneDetectionImpedanceRmsIsValid Ways

Reads the PilotToneDetectionImpedanceRmsIsValid

Offset	Name	Type	Dim	R \ W	Description
0x0000b4b8 (46264)	PilotToneDetectionImpedanceRmsIsValid Ways CH 1	uint32	4	R	Reads the PilotToneDetectionImpedanceRmsIsValid
0x0000b4bc (46268)	PilotToneDetectionImpedanceRmsIsValid Ways CH 2	uint32	4	R	Reads the PilotToneDetectionImpedanceRmsIsValid
0x0000b4c0 (46272)	PilotToneDetectionImpedanceRmsIsValid Ways CH 3	uint32	4	R	Reads the PilotToneDetectionImpedanceRmsIsValid
0x0000b4c4 (46276)	PilotToneDetectionImpedanceRmsIsValid Ways CH 4	uint32	4	R	Reads the PilotToneDetectionImpedanceRmsIsValid

DetectionNominalImpedanceRmsIsValid Ways

Reads the DetectionNominalImpedanceRmsIsValid

Offset	Name	Type	Dim	R \ W	Description
0x0000b4c8 (46280)	DetectionNominalImpedanceRmsIsValid Ways CH 1	uint32	4	R	Reads the DetectionNominalImpedanceRmsIsValid
0x0000b4cc (46284)	DetectionNominalImpedanceRmsIsValid Ways CH 2	uint32	4	R	Reads the DetectionNominalImpedanceRmsIsValid
0x0000b4d0 (46288)	DetectionNominalImpedanceRmsIsValid Ways CH 3	uint32	4	R	Reads the DetectionNominalImpedanceRmsIsValid
0x0000b4d4 (46292)	DetectionNominalImpedanceRmsIsValid Ways CH 4	uint32	4	R	Reads the DetectionNominalImpedanceRmsIsValid

SlowMeter PreWays

This area contains the PreWays slow meters.

BlockId	Start Address	End Address	Description
SlowMeter ExternalGain	0x0000b5e8	0x0000b5f8	This area contains the PreWays ExternalGain.

SlowMeter ExternalGain

This area contains the PreWays ExternalGain.

Offset	Name	Type	Dim	R \ W	Description
0x0000b5e8 (46568)	SlowMeter PreWays CH 1	Float	4	R	Reads the ExternalGain for channel 1
0x0000b5ec (46572)	SlowMeter PreWays CH 2	Float	4	R	Reads the ExternalGain for channel 2
0x0000b5f0 (46576)	SlowMeter PreWays CH 3	Float	4	R	Reads the ExternalGain for channel 3
0x0000b5f4 (46580)	SlowMeter PreWays CH 4	Float	4	R	Reads the ExternalGain for channel 4

SlowMeter Auto Standby

This area contains the Auto Standby slow meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000b638 (46648)	Auto Standby Active	uint32	4	R	Reads the AutoStandby is Active

SlowMeter ChannelAlarmStatuses

This area contains the ChannelAlarmStatuses slow meters.

BlockId	Start Address	End Address	Description
Clip Channel Alarm Statuses	0x0000b63c	0x0000b640	Reads the Clip Alarm
SoaThermal Channel Alarm Statuses	0x0000b640	0x0000b644	Reads the SoaThermal Alarm
Temp Channel Alarm Statuses	0x0000b644	0x0000b648	Reads the Temp Alarm
VRail Channel Alarm Statuses	0x0000b648	0x0000b64c	Reads the VRail Alarm
IAux Channel Alarm Statuses	0x0000b64c	0x0000b650	Reads the IAux Alarm

Clip Channel Alarm Statuses

Reads the Clip Alarm

Offset	Name	Type	Dim	R \ W	Description
0x0000b63c (46652)	Clip CH 1	uint8	1	R	Reads the Clip Alarm for CH 1
0x0000b63d (46653)	Clip CH 2	uint8	1	R	Reads the Clip Alarm for CH 2
0x0000b63e (46654)	Clip CH 3	uint8	1	R	Reads the Clip Alarm for CH 3
0x0000b63f (46655)	Clip CH 4	uint8	1	R	Reads the Clip Alarm for CH 4

SoaThermal Channel Alarm Statuses

Reads the SoaThermal Alarm

Offset	Name	Type	Dim	R \ W	Description
0x0000b640 (46656)	SoaThermal CH 1	uint8	1	R	Reads the SoaThermal Alarm for CH 1
0x0000b641 (46657)	SoaThermal CH 2	uint8	1	R	Reads the SoaThermal Alarm for CH 2
0x0000b642 (46658)	SoaThermal CH 3	uint8	1	R	Reads the SoaThermal Alarm for CH 3
0x0000b643 (46659)	SoaThermal CH 4	uint8	1	R	Reads the SoaThermal Alarm for CH 4

Temp Channel Alarm Statuses

Reads the Temp Alarm

Offset	Name	Type	Dim	R \ W	Description
0x0000b644 (46660)	Temp CH 1	uint8	1	R	Reads the Temp Alarm for CH 1
0x0000b645 (46661)	Temp CH 2	uint8	1	R	Reads the Temp Alarm for CH 2
0x0000b646 (46662)	Temp CH 3	uint8	1	R	Reads the Temp Alarm for CH 3
0x0000b647 (46663)	Temp CH 4	uint8	1	R	Reads the Temp Alarm for CH 4

VRail Channel Alarm Statuses

Reads the VRail Alarm

Offset	Name	Type	Dim	R \ W	Description
0x0000b648 (46664)	VRail CH 1	uint8	1	R	Reads the VRail Alarm for CH 1
0x0000b649 (46665)	VRail CH 2	uint8	1	R	Reads the VRail Alarm for CH 2
0x0000b64a (46666)	VRail CH 3	uint8	1	R	Reads the VRail Alarm for CH 3
0x0000b64b (46667)	VRail CH 4	uint8	1	R	Reads the VRail Alarm for CH 4

IAux Channel Alarm Statuses

Reads the IAux Alarm

Offset	Name	Type	Dim	R \ W	Description
0x0000b64c (46668)	IAux CH 1	uint8	1	R	Reads the IAux Alarm for CH 1
0x0000b64d (46669)	IAux CH 2	uint8	1	R	Reads the IAux Alarm for CH 2
0x0000b64e (46670)	IAux CH 3	uint8	1	R	Reads the IAux Alarm for CH 3
0x0000b64f (46671)	IAux CH 4	uint8	1	R	Reads the IAux Alarm for CH 4

SlowMeter AlarmStatus

This area contains the AlarmStatus slow meters.

Offset	Name	Type	Dim	R	W	Description
0x0000b650 (46672)	Fan	uint8	1	R		Reads the Fan
0x0000b651 (46673)	HighOverTemperature	uint8	1	R		Reads the HighOverTemperature
0x0000b652 (46674)	ModerateOverTemperature	uint8	1	R		Reads the ModerateOverTemperature
0x0000b653 (46675)	PsTempAlarmStatus	uint8	1	R		Reads the PsTempAlarmStatus
0x0000b654 (46676)	VAuxAlarmStatus	uint8	1	R		Reads the VAuxAlarmStatus
0x0000b655 (46677)	GenericFault	uint8	1	R		Generic Fault
0x0000b656 (46678)	Spare2	uint8	1	R		Spare2
0x0000b657 (46679)	Spare3	uint8	1	R		Spare3

Fast Meters

This area contains the fast meters.

BlockId	Start Address	End Address	Description
FastMeter SourceSelection	0x0000b800	0x0000b910	This area contains the SourceSelection fast meters.
FastMeter InputMatrix	0x0000ba40	0x0000ba60	This area contains the InputMatrix fast meters.
FastMeter Ways	0x0000bac0	0x0000bbb8	This area contains the Ways fast meters.
FastMeter PowerSupply	0x0000bbb8	0x0000bbc4	This area contains the PowerSupply fast meters.
FastMeter DspTaskManager	0x0000bbc4	0x0000bbd0	This area contains the DspTaskManager fast meters.

FastMeter SourceSelection

This area contains the SourceSelection fast meters.

BlockId	Start Address	End Address	Description
FastMeter Source Selection Source 1	0x0000b800	0x0000b880	This area contains the source selection fast meter for Source 1.
FastMeter Source Selection Source 2	0x0000b880	0x0000b900	This area contains the source selection fast meter for Source 2.
FastMeter SourceSelection Backup Settings	0x0000b900	0x0000b910	This area contains the Source Selection -> Backup Settings fast meters.

FastMeter Source Selection Source 1

This area contains the source selection fast meter for Source 1.

BlockId	Start Address	End Address	Description
Source 1 Type	0x0000b800	0x0000b808	Reads the Source 1 Type
Source 1 Channel	0x0000b808	0x0000b810	Reads the Source 1 Channel
Source 1 Peak	0x0000b810	0x0000b820	Reads the Source 1 Peak
Source 1 Rms	0x0000b820	0x0000b830	Reads the Source 1 Rms
Source 1 Presence	0x0000b830	0x0000b840	Reads the Source 1 Presence
Source 1 Clip	0x0000b840	0x0000b850	Reads the Source 1 Clip
Source 1 Spare 1	0x0000b850	0x0000b860	Source 1 Spare 1
Source 1 Spare 2	0x0000b860	0x0000b870	Source 1 Spare 2
Source 1 Spare 3	0x0000b870	0x0000b880	Source 1 Spare 3

Source 1 Type

Reads the Source 1 Type

Offset	Name	Type	Dim	R \ W	Description
0x0000b800 (47104)	Source 1 - Type - Channel 1	uint16	2	R	Reads the Source 1 - Type for Channel 1.
0x0000b802 (47106)	Source 1 - Type - Channel 2	uint16	2	R	Reads the Source 1 - Type for Channel 2.
0x0000b804 (47108)	Source 1 - Type - Channel 3	uint16	2	R	Reads the Source 1 - Type for Channel 3.
0x0000b806 (47110)	Source 1 - Type - Channel 4	uint16	2	R	Reads the Source 1 - Type for Channel 4.

Source 1 Channel

Reads the Source 1 Channel

Offset	Name	Type	Dim	R \ W	Description
0x0000b808 (47112)	Source 1 - Channel - Channel 1	uint16	2	R	Reads the Source 1 - Channel for Channel 1.
0x0000b80a (47114)	Source 1 - Channel - Channel 2	uint16	2	R	Reads the Source 1 - Channel for Channel 2.
0x0000b80c (47116)	Source 1 - Channel - Channel 3	uint16	2	R	Reads the Source 1 - Channel for Channel 3.
0x0000b80e (47118)	Source 1 - Channel - Channel 4	uint16	2	R	Reads the Source 1 - Channel for Channel 4.

Source 1 Peak

Reads the Source 1 Peak

Offset	Name	Type	Dim	R \ W	Description
0x0000b810 (47120)	Source 1 - Peak - Channel 1	Float	4	R	Reads the Source 1 - Peak for Channel 1.
0x0000b814 (47124)	Source 1 - Peak - Channel 2	Float	4	R	Reads the Source 1 - Peak for Channel 2.
0x0000b818 (47128)	Source 1 - Peak - Channel 3	Float	4	R	Reads the Source 1 - Peak for Channel 3.
0x0000b81c (47132)	Source 1 - Peak - Channel 4	Float	4	R	Reads the Source 1 - Peak for Channel 4.

Source 1 Rms

Reads the Source 1 Rms

Offset	Name	Type	Dim	R \ W	Description
0x0000b820 (47136)	Source 1 - Rms - Channel 1	Float	4	R	Reads the Source 1 - Rms for Channel 1.
0x0000b824 (47140)	Source 1 - Rms - Channel 2	Float	4	R	Reads the Source 1 - Rms for Channel 2.
0x0000b828 (47144)	Source 1 - Rms - Channel 3	Float	4	R	Reads the Source 1 - Rms for Channel 3.
0x0000b82c (47148)	Source 1 - Rms - Channel 4	Float	4	R	Reads the Source 1 - Rms for Channel 4.

Source 1 Presence

Reads the Source 1 Presence

Offset	Name	Type	Dim	R \ W	Description
0x0000b830 (47152)	Source 1 - Presence - Channel 1	uint32	4	R	Reads the Source 1 - Presence for Channel 1.
0x0000b834 (47156)	Source 1 - Presence - Channel 2	uint32	4	R	Reads the Source 1 - Presence for Channel 2.
0x0000b838 (47160)	Source 1 - Presence - Channel 3	uint32	4	R	Reads the Source 1 - Presence for Channel 3.
0x0000b83c (47164)	Source 1 - Presence - Channel 4	uint32	4	R	Reads the Source 1 - Presence for Channel 4.

Source 1 Clip

Reads the Source 1 Clip

Offset	Name	Type	Dim	R \ W	Description
0x0000b840 (47168)	Source 1 - Clip - Channel 1	uint32	4	R	Reads the Source 1 - Clip for Channel 1.
0x0000b844 (47172)	Source 1 - Clip - Channel 2	uint32	4	R	Reads the Source 1 - Clip for Channel 2.
0x0000b848 (47176)	Source 1 - Clip - Channel 3	uint32	4	R	Reads the Source 1 - Clip for Channel 3.
0x0000b84c (47180)	Source 1 - Clip - Channel 4	uint32	4	R	Reads the Source 1 - Clip for Channel 4.

Source 1 Spare 1

Source 1 Spare 1

Offset	Name	Type	Dim	R \ W	Description
0x0000b850 (47184)	Source 1 - Spare 1 - Channel 1	uint32	4	R	Reads the Source 1 - Spare 1 for Channel 1.
0x0000b854 (47188)	Source 1 - Spare 1 - Channel 2	uint32	4	R	Reads the Source 1 - Spare 1 for Channel 2.
0x0000b858 (47192)	Source 1 - Spare 1 - Channel 3	uint32	4	R	Reads the Source 1 - Spare 1 for Channel 3.
0x0000b85c (47196)	Source 1 - Spare 1 - Channel 4	uint32	4	R	Reads the Source 1 - Spare 1 for Channel 4.

Source 1 Spare 2

Source 1 Spare 2

Offset	Name	Type	Dim	R \ W	Description
0x0000b860 (47200)	Source 1 - Spare 2 - Channel 1	uint32	4	R	Reads the Source 1 - Spare 2 for Channel 1.
0x0000b864 (47204)	Source 1 - Spare 2 - Channel 2	uint32	4	R	Reads the Source 1 - Spare 2 for Channel 2.
0x0000b868 (47208)	Source 1 - Spare 2 - Channel 3	uint32	4	R	Reads the Source 1 - Spare 2 for Channel 3.
0x0000b86c (47212)	Source 1 - Spare 2 - Channel 4	uint32	4	R	Reads the Source 1 - Spare 2 for Channel 4.

Source 1 Spare 3

Source 1 Spare 3

Offset	Name	Type	Dim	R \ W	Description
0x0000b870 (47216)	Source 1 - Spare 3 - Channel 1	uint32	4	R	Reads the Source 1 - Spare 3 for Channel 1.
0x0000b874 (47220)	Source 1 - Spare 3 - Channel 2	uint32	4	R	Reads the Source 1 - Spare 3 for Channel 2.
0x0000b878 (47224)	Source 1 - Spare 3 - Channel 3	uint32	4	R	Reads the Source 1 - Spare 3 for Channel 3.
0x0000b87c (47228)	Source 1 - Spare 3 - Channel 4	uint32	4	R	Reads the Source 1 - Spare 3 for Channel 4.

FastMeter Source Selection Source 2

This area contains the source selection fast meter for Source 2.

BlockId	Start Address	End Address	Description
Source 2 Type	0x0000b880	0x0000b888	Reads the Source 2 Type
Source 2 Channel	0x0000b888	0x0000b890	Reads the Source 2 Channel
Source 2 Peak	0x0000b890	0x0000b8a0	Reads the Source 2 Peak
Source 2 Rms	0x0000b8a0	0x0000b8b0	Reads the Source 2 Rms
Source 2 Presence	0x0000b8b0	0x0000b8c0	Reads the Source 2 Presence
Source 2 Clip	0x0000b8c0	0x0000b8d0	Reads the Source 2 Clip
Source 2 Spare 1	0x0000b8d0	0x0000b8e0	Source 2 Spare 1
Source 2 Spare 2	0x0000b8e0	0x0000b8f0	Source 2 Spare 2
Source 2 Spare 3	0x0000b8f0	0x0000b900	Source 2 Spare 3

Source 2 Type

Reads the Source 2 Type

Offset	Name	Type	Dim	R \ W	Description
0x0000b880 (47232)	Source 2 - Type - Channel 1	uint16	2	R	Reads the Source 2 - Type for Channel 1.
0x0000b882 (47234)	Source 2 - Type - Channel 2	uint16	2	R	Reads the Source 2 - Type for Channel 2.
0x0000b884 (47236)	Source 2 - Type - Channel 3	uint16	2	R	Reads the Source 2 - Type for Channel 3.
0x0000b886 (47238)	Source 2 - Type - Channel 4	uint16	2	R	Reads the Source 2 - Type for Channel 4.

Source 2 Channel

Reads the Source 2 Channel

Offset	Name	Type	Dim	R \ W	Description
0x0000b888 (47240)	Source 2 - Channel - Channel 1	uint16	2	R	Reads the Source 2 - Channel for Channel 1.
0x0000b88a (47242)	Source 2 - Channel - Channel 2	uint16	2	R	Reads the Source 2 - Channel for Channel 2.
0x0000b88c (47244)	Source 2 - Channel - Channel 3	uint16	2	R	Reads the Source 2 - Channel for Channel 3.
0x0000b88e (47246)	Source 2 - Channel - Channel 4	uint16	2	R	Reads the Source 2 - Channel for Channel 4.

Source 2 Peak

Reads the Source 2 Peak

Offset	Name	Type	Dim	R \ W	Description
0x0000b890 (47248)	Source 2 - Peak - Channel 1	Float	4	R	Reads the Source 2 - Peak for Channel 1.
0x0000b894 (47252)	Source 2 - Peak - Channel 2	Float	4	R	Reads the Source 2 - Peak for Channel 2.
0x0000b898 (47256)	Source 2 - Peak - Channel 3	Float	4	R	Reads the Source 2 - Peak for Channel 3.
0x0000b89c (47260)	Source 2 - Peak - Channel 4	Float	4	R	Reads the Source 2 - Peak for Channel 4.

Source 2 Rms

Reads the Source 2 Rms

Offset	Name	Type	Dim	R \ W	Description
0x0000b8a0 (47264)	Source 2 - Rms - Channel 1	Float	4	R	Reads the Source 2 - Rms for Channel 1.
0x0000b8a4 (47268)	Source 2 - Rms - Channel 2	Float	4	R	Reads the Source 2 - Rms for Channel 2.
0x0000b8a8 (47272)	Source 2 - Rms - Channel 3	Float	4	R	Reads the Source 2 - Rms for Channel 3.
0x0000b8ac (47276)	Source 2 - Rms - Channel 4	Float	4	R	Reads the Source 2 - Rms for Channel 4.

Source 2 Presence

Reads the Source 2 Presence

Offset	Name	Type	Dim	R \ W	Description
0x0000b8b0 (47280)	Source 2 - Presence - Channel 1	uint32	4	R	Reads the Source 2 - Presence for Channel 1.
0x0000b8b4 (47284)	Source 2 - Presence - Channel 2	uint32	4	R	Reads the Source 2 - Presence for Channel 2.
0x0000b8b8 (47288)	Source 2 - Presence - Channel 3	uint32	4	R	Reads the Source 2 - Presence for Channel 3.
0x0000b8bc (47292)	Source 2 - Presence - Channel 4	uint32	4	R	Reads the Source 2 - Presence for Channel 4.

Source 2 Clip

Reads the Source 2 Clip

Offset	Name	Type	Dim	R \ W	Description
0x0000b8c0 (47296)	Source 2 - Clip - Channel 1	uint32	4	R	Reads the Source 2 - Clip for Channel 1.
0x0000b8c4 (47300)	Source 2 - Clip - Channel 2	uint32	4	R	Reads the Source 2 - Clip for Channel 2.
0x0000b8c8 (47304)	Source 2 - Clip - Channel 3	uint32	4	R	Reads the Source 2 - Clip for Channel 3.
0x0000b8cc (47308)	Source 2 - Clip - Channel 4	uint32	4	R	Reads the Source 2 - Clip for Channel 4.

Source 2 Spare 1

Source 2 Spare 1

Offset	Name	Type	Dim	R \ W	Description
0x0000b8d0 (47312)	Source 2 - Spare 1 - Channel 1	uint32	4	R	Reads the Source 2 - Spare 1 for Channel 1.
0x0000b8d4 (47316)	Source 2 - Spare 1 - Channel 2	uint32	4	R	Reads the Source 2 - Spare 1 for Channel 2.
0x0000b8d8 (47320)	Source 2 - Spare 1 - Channel 3	uint32	4	R	Reads the Source 2 - Spare 1 for Channel 3.
0x0000b8dc (47324)	Source 2 - Spare 1 - Channel 4	uint32	4	R	Reads the Source 2 - Spare 1 for Channel 4.

Source 2 Spare 2

Source 2 Spare 2

Offset	Name	Type	Dim	R \ W	Description
0x0000b8e0 (47328)	Source 2 - Spare 2 - Channel 1	uint32	4	R	Reads the Source 2 - Spare 2 for Channel 1.
0x0000b8e4 (47332)	Source 2 - Spare 2 - Channel 2	uint32	4	R	Reads the Source 2 - Spare 2 for Channel 2.
0x0000b8e8 (47336)	Source 2 - Spare 2 - Channel 3	uint32	4	R	Reads the Source 2 - Spare 2 for Channel 3.
0x0000b8ec (47340)	Source 2 - Spare 2 - Channel 4	uint32	4	R	Reads the Source 2 - Spare 2 for Channel 4.

Source 2 Spare 3

Source 2 Spare 3

Offset	Name	Type	Dim	R \ W	Description
0x0000b8f0 (47344)	Source 2 - Spare 3 - Channel 1	uint32	4	R	Reads the Source 2 - Spare 3 for Channel 1.
0x0000b8f4 (47348)	Source 2 - Spare 3 - Channel 2	uint32	4	R	Reads the Source 2 - Spare 3 for Channel 2.
0x0000b8f8 (47352)	Source 2 - Spare 3 - Channel 3	uint32	4	R	Reads the Source 2 - Spare 3 for Channel 3.
0x0000b8fc (47356)	Source 2 - Spare 3 - Channel 4	uint32	4	R	Reads the Source 2 - Spare 3 for Channel 4.

FastMeter SourceSelection Backup Settings

This area contains the Source Selection -> Backup Settings fast meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000b900 (47360)	SelectedBackup 1	uint32	4	R	Channel 1 - SelectedBackup
0x0000b904 (47364)	SelectedBackup 2	uint32	4	R	Channel 2 - SelectedBackup
0x0000b908 (47368)	SelectedBackup 3	uint32	4	R	Channel 3 - SelectedBackup
0x0000b90c (47372)	SelectedBackup 4	uint32	4	R	Channel 4 - SelectedBackup

FastMeter InputMatrix

This area contains the InputMatrix fast meters.

BlockId	Start Address	End Address	Description
FastMeter Peak InputMatrix	0x0000ba40	0x0000ba50	This area contains the InputMatrix Peak fast meters.
FastMeter Rms InputMatrix	0x0000ba50	0x0000ba60	This area contains the InputMatrix Rmsfast meters.

FastMeter Peak InputMatrix

This area contains the InputMatrix Peak fast meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000ba40 (47680)	Peak InputMatrix 1	Float	4	R	Reads the FastMeter InputMatrix 1 Peak
0x0000ba44 (47684)	Peak InputMatrix 2	Float	4	R	Reads the FastMeter InputMatrix 2 Peak
0x0000ba48 (47688)	Peak InputMatrix 3	Float	4	R	Reads the FastMeter InputMatrix 3 Peak
0x0000ba4c (47692)	Peak InputMatrix 4	Float	4	R	Reads the FastMeter InputMatrix 4 Peak

FastMeter Rms InputMatrix

This area contains the InputMatrix Rmsfast meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000ba50 (47696)	Rms InputMatrix 1	Float	4	R	Reads the FastMeter InputMatrix 1 Rms
0x0000ba54 (47700)	Rms InputMatrix 2	Float	4	R	Reads the FastMeter InputMatrix 2 Rms
0x0000ba58 (47704)	Rms InputMatrix 3	Float	4	R	Reads the FastMeter InputMatrix 3 Rms
0x0000ba5c (47708)	Rms InputMatrix 4	Float	4	R	Reads the FastMeter InputMatrix 4 Rms

FastMeter Ways

This area contains the Ways fast meters.

BlockId	Start Address	End Address	Description
FastMeter Ways VPeak	0x0000bac0	0x0000bad0	Reads the VPeak - Voltage
FastMeter Ways VRms	0x0000bad0	0x0000bae0	Reads the VRms - Voltage
FastMeter Ways IPeak	0x0000bae0	0x0000baf0	Reads the IPeak - Current
FastMeter Ways IRms	0x0000baf0	0x0000bb00	Reads the IRms - Current
FastMeter Ways NominalImpedance	0x0000bb00	0x0000bb10	Reads the NominalImpedance
FastMeter Ways SoaGainReductionThermalLimiter	0x0000bb10	0x0000bb20	Reads the SoaGainReductionThermalLimiter
FastMeter Ways SoaGainReductionVoltageHflLimiter	0x0000bb20	0x0000bb30	Reads the SoaGainReductionVoltageHflLimiter
FastMeter Ways SoaGainReductionCurrentLongLimiter	0x0000bb30	0x0000bb40	Reads the SoaGainReductionCurrentLongLimiter
FastMeter Ways SoaGainReductionCurrentShortLimiter	0x0000bb40	0x0000bb50	Reads the SoaGainReductionCurrentShortLimiter
FastMeter Ways SoaGainReductionPowerLimiter	0x0000bb50	0x0000bb60	Reads the SoaGainReductionPowerLimiter
FastMeter Ways UserGainReductionVoltageLimiterPeak	0x0000bb60	0x0000bb70	Reads the UserGainReductionVoltageLimiterPeak
FastMeter Ways UserGainReductionVoltageLimiterRms	0x0000bb70	0x0000bb80	Reads the UserGainReductionVoltageLimiterRms
FastMeter Ways UserGainReductionClipLimiter	0x0000bb80	0x0000bb90	Reads the UserGainReductionClipLimiter
FastMeter Ways UserGainReductionTotal	0x0000bb90	0x0000bba0	Reads the UserGainReductionTotal
FastMeter Ways SignalPresence	0x0000bba0	0x0000bba4	Reads the SignalPresence
FastMeter Ways SignalClipping	0x0000bba4	0x0000bba8	Reads the SignalClipping
FastMeter Ways Headroom	0x0000bba8	0x0000bbb8	Reads the Headroom

FastMeter Ways VPeak

Reads the VPeak - Voltage

Offset	Name	Type	Dim	R \ W	Description
0x0000bac0 (47808)	VPeak CH 1	Float	4	R	Reads the VPeak - Voltage for Channel 1 - Voltage
0x0000bac4 (47812)	VPeak CH 2	Float	4	R	Reads the VPeak - Voltage for Channel 2 - Voltage
0x0000bac8 (47816)	VPeak CH 3	Float	4	R	Reads the VPeak - Voltage for Channel 3 - Voltage
0x0000bacc (47820)	VPeak CH 4	Float	4	R	Reads the VPeak - Voltage for Channel 4 - Voltage

FastMeter Ways VRms

Reads the VRms - Voltage

Offset	Name	Type	Dim	R \ W	Description
0x0000bad0 (47824)	VRms CH 1	Float	4	R	Reads the VRms for Channel 1 - Voltage
0x0000bad4 (47828)	VRms CH 2	Float	4	R	Reads the VRms for Channel 2 - Voltage
0x0000bad8 (47832)	VRms CH 3	Float	4	R	Reads the VRms for Channel 3 - Voltage
0x0000badc (47836)	VRms CH 4	Float	4	R	Reads the VRms for Channel 4 - Voltage

FastMeter Ways IPeak

Reads the IPeak - Current

Offset	Name	Type	Dim	R \ W	Description
0x0000bae0 (47840)	IPeak CH 1	Float	4	R	Reads the IPeak for Channel 1 - Current
0x0000bae4 (47844)	IPeak CH 2	Float	4	R	Reads the IPeak for Channel 2 - Current
0x0000bae8 (47848)	IPeak CH 3	Float	4	R	Reads the IPeak for Channel 3 - Current
0x0000baec (47852)	IPeak CH 4	Float	4	R	Reads the IPeak for Channel 4 - Current

FastMeter Ways IRms

Reads the IRms - Current

Offset	Name	Type	Dim	R \ W	Description
0x0000baf0 (47856)	IRms CH 1	Float	4	R	Reads the IRms for Channel 1 - Current
0x0000baf4 (47860)	IRms CH 2	Float	4	R	Reads the IRms for Channel 2 - Current
0x0000baf8 (47864)	IRms CH 3	Float	4	R	Reads the IRms for Channel 3 - Current
0x0000bafc (47868)	IRms CH 4	Float	4	R	Reads the IRms for Channel 4 - Current

FastMeter Ways NominallImpedance

Reads the NominallImpedance

Offset	Name	Type	Dim	R \ W	Description
0x0000bb00 (47872)	NominallImpedance CH 1	Float	4	R	Reads the NominallImpedance for Channel 1
0x0000bb04 (47876)	NominallImpedance CH 2	Float	4	R	Reads the NominallImpedance for Channel 2
0x0000bb08 (47880)	NominallImpedance CH 3	Float	4	R	Reads the NominallImpedance for Channel 3
0x0000bb0c (47884)	NominallImpedance CH 4	Float	4	R	Reads the NominallImpedance for Channel 4

FastMeter Ways SoaGainReductionThermalLimiter

Reads the SoaGainReductionThermalLimiter

Offset	Name	Type	Dim	R \ W	Description
0x0000bb10 (47888)	SoaGainReductionThermalLimiter CH 1	Float	4	R	Reads the SoaGainReductionThermalLimiter for Channel 1
0x0000bb14 (47892)	SoaGainReductionThermalLimiter CH 2	Float	4	R	Reads the SoaGainReductionThermalLimiter for Channel 2
0x0000bb18 (47896)	SoaGainReductionThermalLimiter CH 3	Float	4	R	Reads the SoaGainReductionThermalLimiter for Channel 3
0x0000bb1c (47900)	SoaGainReductionThermalLimiter CH 4	Float	4	R	Reads the SoaGainReductionThermalLimiter for Channel 4

FastMeter Ways SoaGainReductionVoltageHfLimiter

Reads the SoaGainReductionVoltageHfLimiter

Offset	Name	Type	Dim	R \ W	Description
0x0000bb20 (47904)	SoaGainReductionVoltageHfLimiter CH 1	Float	4	R	Reads the SoaGainReductionVoltageHfLimiter for Channel 1
0x0000bb24 (47908)	SoaGainReductionVoltageHfLimiter CH 2	Float	4	R	Reads the SoaGainReductionVoltageHfLimiter for Channel 2
0x0000bb28 (47912)	SoaGainReductionVoltageHfLimiter CH 3	Float	4	R	Reads the SoaGainReductionVoltageHfLimiter for Channel 3
0x0000bb2c (47916)	SoaGainReductionVoltageHfLimiter CH 4	Float	4	R	Reads the SoaGainReductionVoltageHfLimiter for Channel 4

FastMeter Ways SoaGainReductionCurrentLongLimiter

Reads the SoaGainReductionCurrentLongLimiter

Offset	Name	Type	Dim	R \ W	Description
0x0000bb30 (47920)	SoaGainReductionCurrentLongLimiter CH 1	Float	4	R	Reads the SoaGainReductionCurrentLongLimiter for Channel 1
0x0000bb34 (47924)	SoaGainReductionCurrentLongLimiter CH 2	Float	4	R	Reads the SoaGainReductionCurrentLongLimiter for Channel 2
0x0000bb38 (47928)	SoaGainReductionCurrentLongLimiter CH 3	Float	4	R	Reads the SoaGainReductionCurrentLongLimiter for Channel 3
0x0000bb3c (47932)	SoaGainReductionCurrentLongLimiter CH 4	Float	4	R	Reads the SoaGainReductionCurrentLongLimiter for Channel 4

FastMeter Ways SoaGainReductionCurrentShortLimiter

Reads the SoaGainReductionCurrentShortLimiter

Offset	Name	Type	Dim	R \ W	Description
0x0000bb40 (47936)	SoaGainReductionCurrentShortLimiter CH 1	Float	4	R	Reads the SoaGainReductionCurrentShortLimiter for Channel 1
0x0000bb44 (47940)	SoaGainReductionCurrentShortLimiter CH 2	Float	4	R	Reads the SoaGainReductionCurrentShortLimiter for Channel 2
0x0000bb48 (47944)	SoaGainReductionCurrentShortLimiter CH 3	Float	4	R	Reads the SoaGainReductionCurrentShortLimiter for Channel 3
0x0000bb4c (47948)	SoaGainReductionCurrentShortLimiter CH 4	Float	4	R	Reads the SoaGainReductionCurrentShortLimiter for Channel 4

FastMeter Ways SoaGainReductionPowerLimiter

Reads the SoaGainReductionPowerLimiter

Offset	Name	Type	Dim	R \ W	Description
0x0000bb50 (47952)	SoaGainReductionPowerLimiter CH 1	Float	4	R	Reads the SoaGainReductionPowerLimiter for Channel 1
0x0000bb54 (47956)	SoaGainReductionPowerLimiter CH 2	Float	4	R	Reads the SoaGainReductionPowerLimiter for Channel 2
0x0000bb58 (47960)	SoaGainReductionPowerLimiter CH 3	Float	4	R	Reads the SoaGainReductionPowerLimiter for Channel 3
0x0000bb5c (47964)	SoaGainReductionPowerLimiter CH 4	Float	4	R	Reads the SoaGainReductionPowerLimiter for Channel 4

FastMeter Ways UserGainReductionVoltageLimiterPeak

Reads the UserGainReductionVoltageLimiterPeak

Offset	Name	Type	Dim	R \ W	Description
0x0000bb60 (47968)	UserGainReductionVoltageLimiterPeak CH 1	Float	4	R	Reads the UserGainReductionVoltageLimiterPeak for Channel 1
0x0000bb64 (47972)	UserGainReductionVoltageLimiterPeak CH 2	Float	4	R	Reads the UserGainReductionVoltageLimiterPeak for Channel 2
0x0000bb68 (47976)	UserGainReductionVoltageLimiterPeak CH 3	Float	4	R	Reads the UserGainReductionVoltageLimiterPeak for Channel 3
0x0000bb6c (47980)	UserGainReductionVoltageLimiterPeak CH 4	Float	4	R	Reads the UserGainReductionVoltageLimiterPeak for Channel 4

FastMeter Ways UserGainReductionVoltageLimiterRms

Reads the UserGainReductionVoltageLimiterRms

Offset	Name	Type	Dim	R \ W	Description
0x0000bb70 (47984)	UserGainReductionVoltageLimiterRms CH 1	Float	4	R	Reads the UserGainReductionVoltageLimiterRms for Channel 1
0x0000bb74 (47988)	UserGainReductionVoltageLimiterRms CH 2	Float	4	R	Reads the UserGainReductionVoltageLimiterRms for Channel 2
0x0000bb78 (47992)	UserGainReductionVoltageLimiterRms CH 3	Float	4	R	Reads the UserGainReductionVoltageLimiterRms for Channel 3
0x0000bb7c (47996)	UserGainReductionVoltageLimiterRms CH 4	Float	4	R	Reads the UserGainReductionVoltageLimiterRms for Channel 4

FastMeter Ways UserGainReductionClipLimiter

Reads the UserGainReductionClipLimiter

Offset	Name	Type	Dim	R \ W	Description
0x0000bb80 (48000)	UserGainReductionClipLimiter CH 1	Float	4	R	Reads the UserGainReductionClipLimiter for Channel 1
0x0000bb84 (48004)	UserGainReductionClipLimiter CH 2	Float	4	R	Reads the UserGainReductionClipLimiter for Channel 2
0x0000bb88 (48008)	UserGainReductionClipLimiter CH 3	Float	4	R	Reads the UserGainReductionClipLimiter for Channel 3
0x0000bb8c (48012)	UserGainReductionClipLimiter CH 4	Float	4	R	Reads the UserGainReductionClipLimiter for Channel 4

FastMeter Ways UserGainReductionTotal

Reads the UserGainReductionTotal

Offset	Name	Type	Dim	R \ W	Description
0x0000bb90 (48016)	UserGainReductionTotal CH 1	Float	4	R	Reads the UserGainReductionTotal for Channel 1
0x0000bb94 (48020)	UserGainReductionTotal CH 2	Float	4	R	Reads the UserGainReductionTotal for Channel 2
0x0000bb98 (48024)	UserGainReductionTotal CH 3	Float	4	R	Reads the UserGainReductionTotal for Channel 3
0x0000bb9c (48028)	UserGainReductionTotal CH 4	Float	4	R	Reads the UserGainReductionTotal for Channel 4

FastMeter Ways SignalPresence

Reads the SignalPresence

Offset	Name	Type	Dim	R \ W	Description
0x0000bba0 (48032)	SignalPresence CH 1	uint8	1	R	Reads the SignalPresence for Channel 1
0x0000bba1 (48033)	SignalPresence CH 2	uint8	1	R	Reads the SignalPresence for Channel 2
0x0000bba2 (48034)	SignalPresence CH 3	uint8	1	R	Reads the SignalPresence for Channel 3
0x0000bba3 (48035)	SignalPresence CH 4	uint8	1	R	Reads the SignalPresence for Channel 4

FastMeter Ways SignalClipping

Reads the SignalClipping

Offset	Name	Type	Dim	R \ W	Description
0x0000bba4 (48036)	SignalClipping CH 1	uint8	1	R	Reads the SignalClipping for Channel 1
0x0000bba5 (48037)	SignalClipping CH 2	uint8	1	R	Reads the SignalClipping for Channel 2
0x0000bba6 (48038)	SignalClipping CH 3	uint8	1	R	Reads the SignalClipping for Channel 3
0x0000bba7 (48039)	SignalClipping CH 4	uint8	1	R	Reads the SignalClipping for Channel 4

FastMeter Ways Headroom

Reads the Headroom

Offset	Name	Type	Dim	R \ W	Description
0x0000bba8 (48040)	Headroom CH 1	Float	4	R	Reads the Headroom for Channel 1
0x0000bbac (48044)	Headroom CH 2	Float	4	R	Reads the Headroom for Channel 2
0x0000bbb0 (48048)	Headroom CH 3	Float	4	R	Reads the Headroom for Channel 3
0x0000bbb4 (48052)	Headroom CH 4	Float	4	R	Reads the Headroom for Channel 4

FastMeter PowerSupply

This area contains the PowerSupply fast meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000bbb8 (48056)	SoaGainReductionPowerLimiter	Float	4	R	Reads the SoaGainReductionPowerLimiter
0x0000bbbc (48060)	SoaGainReductionTempTrLimiter	Float	4	R	Reads the SoaGainReductionTempTrLimiter
0x0000bbc0 (48064)	SoaGainReductionTempHSLimiter	Float	4	R	Reads the SoaGainReductionTempHSLimiter Heat Sink

FastMeter DspTaskManager

This area contains the DspTaskManager fast meters.

Offset	Name	Type	Dim	R \ W	Description
0x0000bbc4 (48068)	AverageUsage	Float	4	R	Reads the AverageUsage
0x0000bbc8 (48072)	PeakUsage	Float	4	R	Reads the PeakUsage
0x0000bbcc (48076)	OperatingState	uint32	4	R	Reads the OperatingState Status -> Warning color

AutoSetup

This area contains informations related to the autosetup.

BlockId	Start Address	End Address	Description
AutoSetup Parameters Way 1	0x0000c000	0x0000c00c	This area contains informations related to the autosetup.
AutoSetup Parameters Way 2	0x0000c00c	0x0000c018	This area contains informations related to the autosetup.
AutoSetup Parameters Way 3	0x0000c018	0x0000c024	This area contains informations related to the autosetup.
AutoSetup Parameters Way 4	0x0000c024	0x0000c030	This area contains informations related to the autosetup.
AutoSetup Results Way 1	0x0000c100	0x0000c79c	This area contains informations related to result.
AutoSetup Results Way 2	0x0000c79c	0x0000ce38	This area contains informations related to result.
AutoSetup Results Way 3	0x0000ce38	0x0000d4d4	This area contains informations related to result.
AutoSetup Results Way 4	0x0000d4d4	0x0000db70	This area contains informations related to result.
AutoSetup Start	0x0000ef00	0x0000ef04	This area contains informations related to result.

AutoSetup Parameters Way 1

This area contains informations related to the autosetup.

Offset	Name	Type	Dim	R \ W	Description
0x0000c000 (49152)	AutoSetup flag Parameter Way 1	uint32	4	RW	AutoSetup bitwise flags:, first flag is used for 'apply' autosetup
0x0000c004 (49156)	Speaker Voltage Way 1	uint32	4	RW	AutoSetup speaker voltage
0x0000c008 (49160)	Sensitivity 1	float	4	RW	AutoSetup sensitivity

AutoSetup Parameters Way 2

This area contains informations related to the autosetup.

Offset	Name	Type	Dim	R \ W	Description
0x0000c00c (49164)	AutoSetup flag Parameter Way 2	uint32	4	RW	AutoSetup bitwise flags:, first flag is used for `apply` autosetup
0x0000c010 (49168)	Speaker Voltage Way 2	uint32	4	RW	AutoSetup speaker voltage
0x0000c014 (49172)	Sensitivity 2	float	4	RW	AutoSetup sensitivity

AutoSetup Parameters Way 3

This area contains informations related to the autosetup.

Offset	Name	Type	Dim	R \ W	Description
0x0000c018 (49176)	AutoSetup flag Parameter Way 3	uint32	4	RW	AutoSetup bitwise flags:, first flag is used for `apply` autosetup
0x0000c01c (49180)	Speaker Voltage Way 3	uint32	4	RW	AutoSetup speaker voltage
0x0000c020 (49184)	Sensitivity 3	float	4	RW	AutoSetup sensitivity

AutoSetup Parameters Way 4

This area contains informations related to the autosetup.

Offset	Name	Type	Dim	R \ W	Description
0x0000c024 (49188)	AutoSetup flag Parameter Way 4	uint32	4	RW	AutoSetup bitwise flags:, first flag is used for `apply` autosetup
0x0000c028 (49192)	Speaker Voltage Way 4	uint32	4	RW	AutoSetup speaker voltage
0x0000c02c (49196)	Sensitivity 4	float	4	RW	AutoSetup sensitivity

AutoSetup Results Way 1

This area contains informations related to result.

BlockId	Start Address	End Address	Description
AutoSetup Settings Result	0x0000c100	0x0000c134	AutoSetup Settings Result
AutoSetup Impedance Result	0x0000c134	0x0000c79c	AutoSetup Impedance Result

AutoSetup Settings Result

AutoSetup Settings Result

Offset	Name	Type	Dim	R \ W	Description
0x0000c100 (49408)	AutoSetup impMeasChannelDone 1	uint32	4	R\W	AutoSetup impMeasChannelDone
0x0000c104 (49412)	AutoSetup stateResult 1	uint32	4	R\W	AutoSetup stateResult
0x0000c108 (49416)	AutoSetup gain 1	float	4	R\W	AutoSetup gain
0x0000c10c (49420)	AutoSetup hiPassFc 1	float	4	R\W	AutoSetup hiPassFc
0x0000c110 (49424)	AutoSetup hiPass Slope 1	uint32	4	R\W	AutoSetup hiPass Slope
0x0000c114 (49428)	AutoSetup hiPass type 1	uint32	4	R\W	AutoSetup hiPass type
0x0000c118 (49432)	AutoSetup zNom 1	float	4	R\W	AutoSetup zNom
0x0000c11c (49436)	AutoSetup peakLimiterThr 1	float	4	R\W	AutoSetup peakLimiterThr
0x0000c120 (49440)	AutoSetup peakLimiterAttackTime 1	float	4	R\W	AutoSetup peakLimiterAttackTime
0x0000c124 (49444)	AutoSetup peakLimiterReleaseTime 1	float	4	R\W	AutoSetup peakLimiterReleaseTime
0x0000c128 (49448)	AutoSetup rmsLimiterThr 1	float	4	R\W	AutoSetup rmsLimiterThr
0x0000c12c (49452)	AutoSetup rmsLimiterAttackTime 1	float	4	R\W	AutoSetup rmsLimiterAttackTime
0x0000c130 (49456)	AutoSetup rmsLimiterReleaseTime 1	float	4	R\W	AutoSetup rmsLimiterReleaseTime

AutoSetup Impedance Result

AutoSetup Impedance Result

Offset	Name	Type	Dim	R \ W	Description
0x0000c134 (49460)	AutoSetup Impedance Channel	uint32	4	R\W	AutoSetup Impedance Channel
0x0000c138 (49464)	AutoSetup Impedance NumFreq	uint32	4	R\W	AutoSetup Impedance NumFreq
0x0000c13c (49468)	AutoSetup Impedance Freq	float[136]	544	R\W	AutoSetup Impedance Freq
0x0000c35c (50012)	AutoSetup Impedance Z	complex_t[136]	1088	R\W	AutoSetup Impedance Z

AutoSetup Results Way 2

This area contains informations related to result.

BlockId	Start Address	End Address	Description
AutoSetup Settings Result	0x0000c79c	0x0000c7d0	AutoSetup Settings Result
AutoSetup Impedance Result	0x0000c7d0	0x0000ce38	AutoSetup Impedance Result

AutoSetup Settings Result

AutoSetup Settings Result

Offset	Name	Type	Dim	R \ W	Description
0x0000c79c (51100)	AutoSetup impMeasChannelDone 1	uint32	4	R\W	AutoSetup impMeasChannelDone
0x0000c7a0 (51104)	AutoSetup stateResult 1	uint32	4	R\W	AutoSetup stateResult
0x0000c7a4 (51108)	AutoSetup gain 1	float	4	R\W	AutoSetup gain
0x0000c7a8 (51112)	AutoSetup hiPassFc 1	float	4	R\W	AutoSetup hiPassFc
0x0000c7ac (51116)	AutoSetup hiPass Slope 1	uint32	4	R\W	AutoSetup hiPass Slope
0x0000c7b0 (51120)	AutoSetup hiPass type 1	uint32	4	R\W	AutoSetup hiPass type
0x0000c7b4 (51124)	AutoSetup zNom 1	float	4	R\W	AutoSetup zNom
0x0000c7b8 (51128)	AutoSetup peakLimiterThr 1	float	4	R\W	AutoSetup peakLimiterThr
0x0000c7bc (51132)	AutoSetup peakLimiterAttackTime 1	float	4	R\W	AutoSetup peakLimiterAttackTime
0x0000c7c0 (51136)	AutoSetup peakLimiterReleaseTime 1	float	4	R\W	AutoSetup peakLimiterReleaseTime
0x0000c7c4 (51140)	AutoSetup rmsLimiterThr 1	float	4	R\W	AutoSetup rmsLimiterThr
0x0000c7c8 (51144)	AutoSetup rmsLimiterAttackTime 1	float	4	R\W	AutoSetup rmsLimiterAttackTime
0x0000c7cc (51148)	AutoSetup rmsLimiterReleaseTime 1	float	4	R\W	AutoSetup rmsLimiterReleaseTime

AutoSetup Impedance Result

AutoSetup Impedance Result

Offset	Name	Type	Dim	R \ W	Description
0x0000c7d0 (51152)	AutoSetup Impedance Channel	uint32	4	R\W	AutoSetup Impedance Channel
0x0000c7d4 (51156)	AutoSetup Impedance NumFreq	uint32	4	R\W	AutoSetup Impedance NumFreq
0x0000c7d8 (51160)	AutoSetup Impedance Freq	float[136]	544	R\W	AutoSetup Impedance Freq
0x0000c9f8 (51704)	AutoSetup Impedance Z	complex_t[136]	1088	R\W	AutoSetup Impedance Z

AutoSetup Results Way 3

This area contains informations related to result.

BlockId	Start Address	End Address	Description
AutoSetup Settings Result	0x0000ce38	0x0000ce6c	AutoSetup Settings Result
AutoSetup Impedance Result	0x0000ce6c	0x0000d4d4	AutoSetup Impedance Result

AutoSetup Settings Result

AutoSetup Settings Result

Offset	Name	Type	Dim	R \ W	Description
0x0000ce38 (52792)	AutoSetup impMeasChannelDone 1	uint32	4	R\W	AutoSetup impMeasChannelDone
0x0000ce3c (52796)	AutoSetup stateResult 1	uint32	4	R\W	AutoSetup stateResult
0x0000ce40 (52800)	AutoSetup gain 1	float	4	R\W	AutoSetup gain
0x0000ce44 (52804)	AutoSetup hiPassFc 1	float	4	R\W	AutoSetup hiPassFc
0x0000ce48 (52808)	AutoSetup hiPass Slope 1	uint32	4	R\W	AutoSetup hiPass Slope
0x0000ce4c (52812)	AutoSetup hiPass type 1	uint32	4	R\W	AutoSetup hiPass type
0x0000ce50 (52816)	AutoSetup zNom 1	float	4	R\W	AutoSetup zNom
0x0000ce54 (52820)	AutoSetup peakLimiterThr 1	float	4	R\W	AutoSetup peakLimiterThr
0x0000ce58 (52824)	AutoSetup peakLimiterAttackTime 1	float	4	R\W	AutoSetup peakLimiterAttackTime
0x0000ce5c (52828)	AutoSetup peakLimiterReleaseTime 1	float	4	R\W	AutoSetup peakLimiterReleaseTime
0x0000ce60 (52832)	AutoSetup rmsLimiterThr 1	float	4	R\W	AutoSetup rmsLimiterThr
0x0000ce64 (52836)	AutoSetup rmsLimiterAttackTime 1	float	4	R\W	AutoSetup rmsLimiterAttackTime
0x0000ce68 (52840)	AutoSetup rmsLimiterReleaseTime 1	float	4	R\W	AutoSetup rmsLimiterReleaseTime

AutoSetup Impedance Result

AutoSetup Impedance Result

Offset	Name	Type	Dim	R \ W	Description
0x0000ce6c (52844)	AutoSetup Impedance Channel	uint32	4	R\W	AutoSetup Impedance Channel
0x0000ce70 (52848)	AutoSetup Impedance NumFreq	uint32	4	R\W	AutoSetup Impedance NumFreq
0x0000ce74 (52852)	AutoSetup Impedance Freq	float[136]	544	R\W	AutoSetup Impedance Freq
0x0000d094 (53396)	AutoSetup Impedance Z	complex_t[136]	1088	R\W	AutoSetup Impedance Z

AutoSetup Results Way 4

This area contains informations related to result.

BlockId	Start Address	End Address	Description
AutoSetup Settings Result	0x0000d4d4	0x0000d508	AutoSetup Settings Result
AutoSetup Impedance Result	0x0000d508	0x0000db70	AutoSetup Impedance Result

AutoSetup Settings Result

AutoSetup Settings Result

Offset	Name	Type	Dim	R \ W	Description
0x0000d4d4 (54484)	AutoSetup impMeasChannelDone 1	uint32	4	R/W	AutoSetup impMeasChannelDone
0x0000d4d8 (54488)	AutoSetup stateResult 1	uint32	4	R/W	AutoSetup stateResult
0x0000d4dc (54492)	AutoSetup gain 1	float	4	R/W	AutoSetup gain
0x0000d4e0 (54496)	AutoSetup hiPassFc 1	float	4	R/W	AutoSetup hiPassFc
0x0000d4e4 (54500)	AutoSetup hiPass Slope 1	uint32	4	R/W	AutoSetup hiPass Slope
0x0000d4e8 (54504)	AutoSetup hiPass type 1	uint32	4	R/W	AutoSetup hiPass type
0x0000d4ec (54508)	AutoSetup zNom 1	float	4	R/W	AutoSetup zNom
0x0000d4f0 (54512)	AutoSetup peakLimiterThr 1	float	4	R/W	AutoSetup peakLimiterThr
0x0000d4f4 (54516)	AutoSetup peakLimiterAttackTime 1	float	4	R/W	AutoSetup peakLimiterAttackTime
0x0000d4f8 (54520)	AutoSetup peakLimiterReleaseTime 1	float	4	R/W	AutoSetup peakLimiterReleaseTime
0x0000d4fc (54524)	AutoSetup rmsLimiterThr 1	float	4	R/W	AutoSetup rmsLimiterThr
0x0000d500 (54528)	AutoSetup rmsLimiterAttackTime 1	float	4	R/W	AutoSetup rmsLimiterAttackTime
0x0000d504 (54532)	AutoSetup rmsLimiterReleaseTime 1	float	4	R/W	AutoSetup rmsLimiterReleaseTime

AutoSetup Impedance Result

AutoSetup Impedance Result

Offset	Name	Type	Dim	R \ W	Description
0x0000d508 (54536)	AutoSetup Impedance Channel	uint32	4	R/W	AutoSetup Impedance Channel
0x0000d50c (54540)	AutoSetup Impedance NumFreq	uint32	4	R/W	AutoSetup Impedance NumFreq
0x0000d510 (54544)	AutoSetup Impedance Freq	float[136]	544	R/W	AutoSetup Impedance Freq
0x0000d730 (55088)	AutoSetup Impedance Z	complex_t[136]	1088	R/W	AutoSetup Impedance Z

AutoSetup Start

This area contains informations related to result.

Offset	Name	Type	Dim	R \ W	Description
0x0000ef00 (61184)	AutoSetup Start on channel	uint32	4	R/W	Write here the channel where init the AutoSetup -> eg: '0' '1' '2' '3'

Blink

The blink command

Offset	Name	Type	Dim	R \ W	Description
0x00100000 (1048576)	Blink	uint8	1	R/W	If set to `1` starts blinking, if set to `0` stops blinking

System Reboot

The system reboot command

Offset	Name	Type	Dim	R \ W	Description
0x00100001 (1048577)	System Reboot	uint8	1	R/W	If set to `0xAC` reboots the device

Firmware Area

This area contains the firmware. The max size for firmware is 1048576

Offset	Name	Type	Dim	R \ W	Description
0x00700000 (7340032)	Firmware	uint8[]	1048576	R/W	Reads the TempTrasf

Firmware Start

This area contains the new firmware information, use this to verify the firmware before flash it

Offset	Name	Type	Dim	R \ W	Description
0x00900000 (9437184)	Firmware CRC	uint16	2	R/W	The firmware crc
0x00900002 (9437186)	Firmware size	uint32	4	R/W	The firmware size

Firmware Flash Erase

This will start the upgrade firmware

Offset	Name	Type	Dim	R \ W	Description
0x00900010 (9437200)	Start upgrade	uint8	1	R/W	Starts the firmware upgrade

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